# CFP2000 Result

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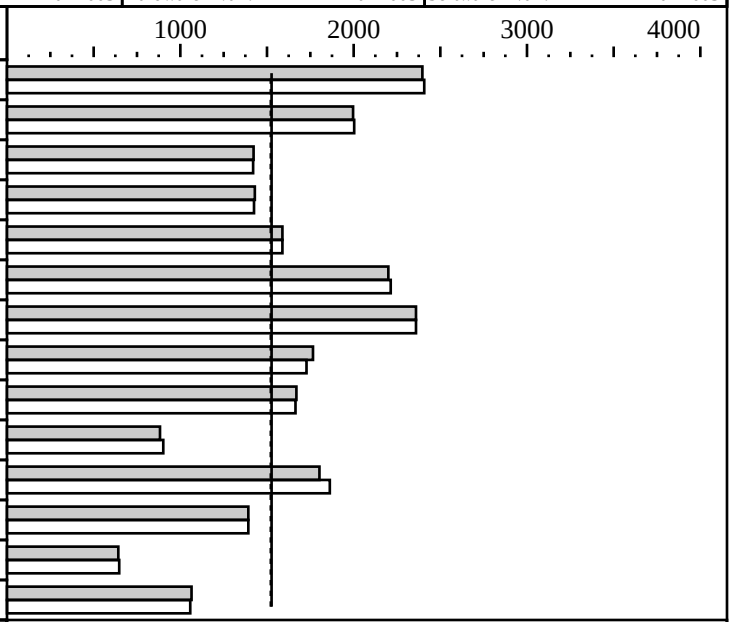
## Acer Incorporated
## Altos R510 (3.6 GHz Intel Xeon)

SPECfp2000 = 1526

SPECfp_base2000 = 1522

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Mar-2005 | Hardware Avail: Mar-2005 | Software Avail: Mar-2005

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |
|---|---|---|---|---|---|
| 168.wupwise | 1600 | 66.8 | 2396 | 66.5 | 2407 |
| 171.swim | 3100 | 155 | 1997 | 155 | 2003 |
| 172.mgrid | 1800 | 127 | 1423 | 127 | 1420 |
| 173.applu | 2100 | 147 | 1430 | 147 | 1425 |
| 177.mesa | 1400 | 88.1 | 1589 | 88.1 | 1589 |
| 178.galgel | 2900 | 132 | 2200 | 131 | 2214 |
| 179.art | 2600 | 110 | 2360 | 110 | 2360 |
| 183.equake | 1300 | 73.6 | 1765 | 75.2 | 1728 |
| 187.facerec | 1900 | 114 | 1670 | 114 | 1664 |
| 188.ammp | 2200 | 249 | 883 | 244 | 902 |
| 189.lucas | 2000 | 111 | 1803 | 107 | 1862 |
| 191.fma3d | 2100 | 151 | 1392 | 151 | 1392 |
| 200.sixtrack | 1100 | 171 | 642 | 170 | 647 |
| 301.apsi | 2600 | 244 | 1065 | 246 | 1057 |




### Hardware

| | |
|---|---|
| CPU: | Intel Xeon (3.6 GHz, 1MB L2, 800MHz system bus) |
| CPU MHz: | 3600 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled) |
| CPU(s) orderable: | 1,2 |
| Parallel: | No |
| Primary Cache: | 12KB(I) micro-ops + 16KB(D) on chip |
| Secondary Cache: | 1024KB(I+D) on chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 6x512MB DDR-333 ECC SDRAM |
| Disk Subsystem: | 1 x 80GB 7200RPM SATA HDD |
| Other Hardware: | |

### Software

| | |
|---|---|
| Operating System: | Microsoft Windows Server 2003 Enterprise Edition (Build 3790) |
| Compiler: | Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
+FDO: PASS1=  -Qprof_gen  PASS2=-Qprof_use
Base tuning:  -fast -Qansi_alias +FDO
Portability:
178.galgel:   -FI /F32000000
Peak tuning:
168.wupwise:  -fast -Qansi_alias +FDO
171.swim:     -fast -Qansi_alias +FDO
172.mgrid:    -fast -Qansi_alias +FDO
173.applu:  -fast -Qscalar_rep- -Qauto  +FDO
177.mesa:   basepeak=yes
178.galgel:   -fast -Qansi_alias +FDO
179.art:      basepeak=yes
183.equake:   -QxP -Oa -Qrcd -Qipo +FDO
187.facerec:  -fast -Qunroll1 -Qscalar_rep- +FDO
188.ammp:     -Oa -QxP
189.lucas:    -fast -Qprefetch- +FDO
191.fma3d:    basepeak=yes
```

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## Notes/Tuning Information (Continued)

```
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located as of Apr-2005:
http://www.acer.com
```