CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

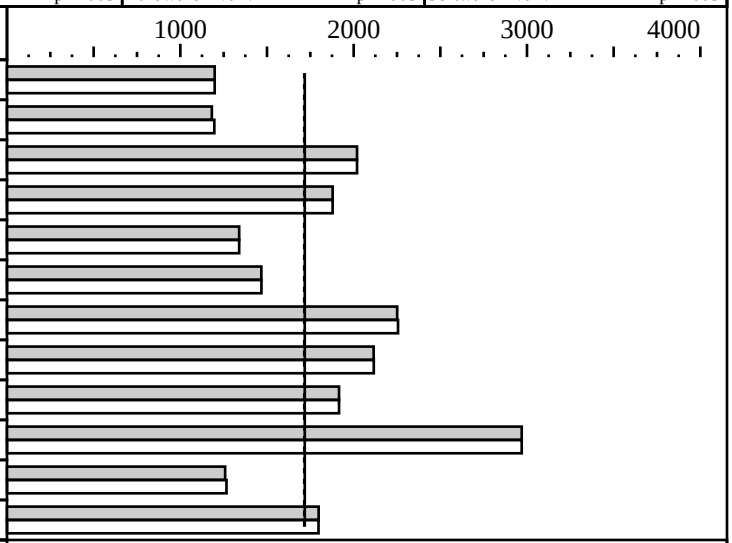
Acer Incorporated
Altos R710 (3.6 GHz Intel Xeon)

SPECint2000 = 1718

SPECint_base2000 = 1715

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Apr-2005 | Hardware Avail: Apr-2005 | Software Avail: Apr-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	117	1198	117	1198
175.vpr	1400	118	1182	117	1196
176.gcc	1100	54.5	2019	54.5	2019
181.mcf	1800	95.8	1879	95.8	1879
186.crafty	1000	74.7	1339	74.7	1339
197.parser	1800	123	1467	123	1468
252.eon	1300	57.8	2251	57.6	2256
253.perlbmk	1800	85.1	2115	85.1	2116
254.gap	1100	57.4	1916	57.4	1916
255.vortex	1900	64.0	2969	64.0	2969
256.bzip2	1500	119	1259	118	1266
300.twolf	3000	167	1798	167	1797



Hardware

CPU:	Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 36GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Compiler 8.1 Build 20041019Z Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast -O3 +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```