CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

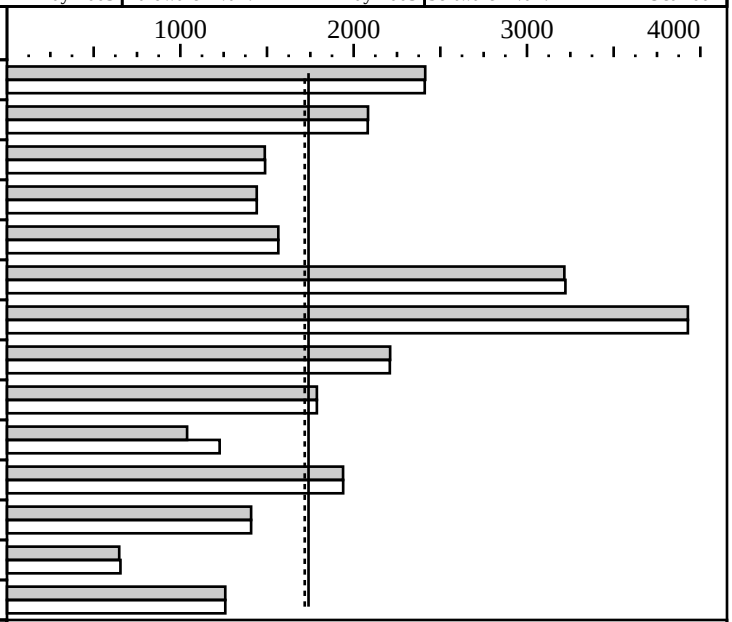
Acer Incorporated
Altos R510 (3.6 GHz Intel Xeon)

SPECfp2000 = 1740

SPECfp_base2000 = 1718

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2005 | Hardware Avail: May-2005 | Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	66.3	2413	66.4	2410
171.swim	3100	149	2083	149	2081
172.mgrid	1800	121	1487	121	1489
173.applu	2100	146	1441	146	1441
177.mesa	1400	89.4	1565	89.4	1565
178.galgel	2900	90.2	3216	90.0	3222
179.art	2600	66.2	3928	66.2	3928
183.equake	1300	58.8	2210	58.9	2209
187.facerec	1900	106	1788	106	1788
188.ammp	2200	212	1039	179	1227
189.lucas	2000	103	1939	103	1939
191.fma3d	2100	149	1408	149	1408
200.sixtrack	1100	170	647	168	655
301.apsi	2600	206	1259	206	1259



Hardware

CPU:	Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 512MB ECC DDR-333 SDRAM
Disk Subsystem:	1 x 120GB 7200RPM SATA HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=  -Qprof_gen  PASS2=-Qprof_use
Base tuning for Fortran Programs :  -fast -Qansi_alias +FDO
177.mesa:Base for C Program: -fast +FDO shlw32M.lib
179.art:Base for C Program: -fast +FDO shlw32M.lib
183.equake:Base for C Program: -fast +FDO shlw32M.lib
188.ammp:Base for C Program: -fast +FDO shlw32M.lib
Portability:
178.galgel:   -FI /F32000000
Peak tuning:
168.wupwise:  -fast -Qansi_alias +FDO
171.swim:     -fast -Qansi_alias +FDO
172.mgrid:    -fast -Qansi_alias +FDO
173.applu:  basepeak=yes
177.mesa:   basepeak=yes
178.galgel:   -fast -Qansi_alias +FDO
179.art:      basepeak=yes
183.equake:   -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
187.facerec:  basepeak=yes
188.ammp:     -Oa -QxP
189.lucas:    -fast -Qprefetch- +FDO
191.fma3d:    basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```