CINT2000 Result

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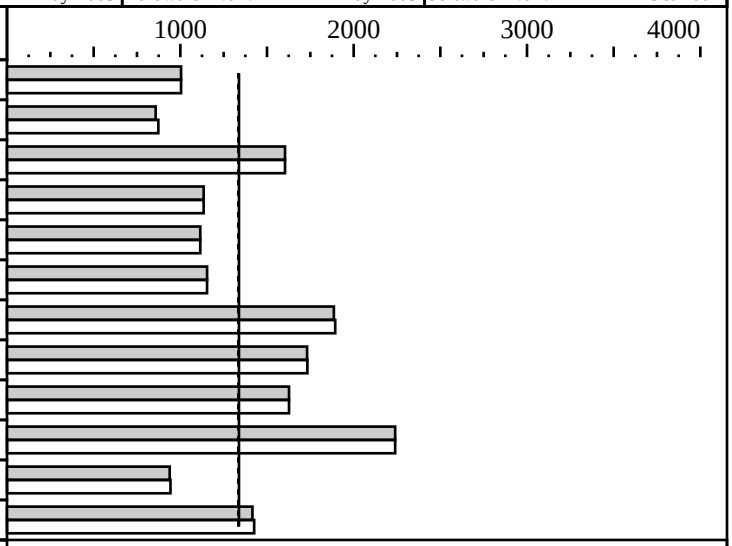
Acer Incorporated
Altos R710 (3.0 GHz Intel Xeon)

SPECint2000 = 1338

SPECint_base2000 = 1335

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2005 | Hardware Avail: May-2005 | Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	139	1004	139	1004
175.vpr	1400	163	858	160	873
176.gcc	1100	68.6	1604	68.6	1604
181.mcf	1800	159	1135	159	1135
186.crafty	1000	89.7	1115	89.7	1115
197.parser	1800	156	1154	156	1154
252.eon	1300	68.9	1886	68.7	1893
253.perlbmk	1800	104	1731	104	1733
254.gap	1100	67.6	1627	67.6	1627
255.vortex	1900	84.8	2239	84.8	2239
256.bzip2	1500	160	939	159	943
300.twolf	3000	212	1416	210	1426



Hardware

CPU:	Intel Xeon (3.0 GHz, 1MB L2, 800MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 73GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Compiler 8.1 Build 20041019Z Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

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Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast -O3 +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```