CFP2000 Result

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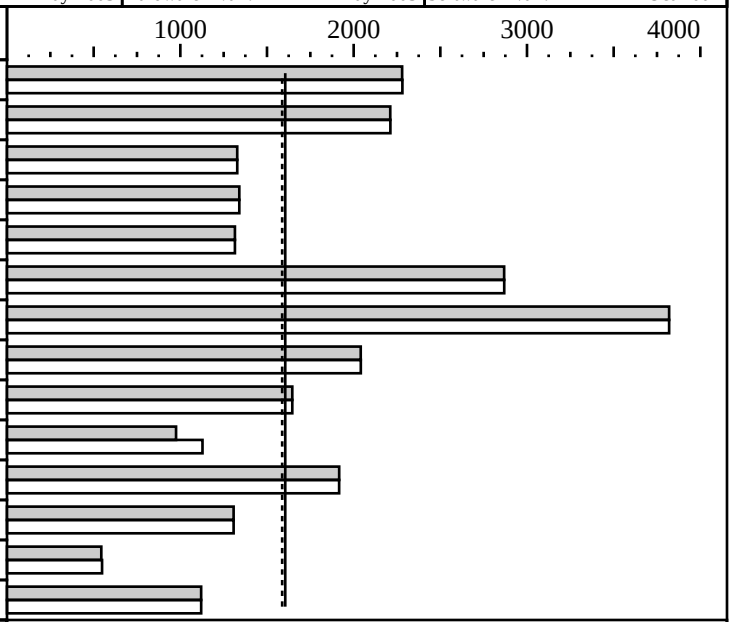
Acer Incorporated
Altos R710 (3.0 GHz Intel Xeon)

SPECfp2000 = 1605

SPECfp_base2000 = 1588

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2005 | Hardware Avail: May-2005 | Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	70.2	2280	70.1	2281
171.swim	3100	140	2211	140	2212
172.mgrid	1800	136	1328	136	1328
173.applu	2100	157	1340	157	1340
177.mesa	1400	106	1315	106	1315
178.galgel	2900	101	2868	101	2869
179.art	2600	68.1	3820	68.1	3820
183.equake	1300	63.7	2041	63.7	2041
187.facerec	1900	115	1646	115	1646
188.ammp	2200	226	975	195	1128
189.lucas	2000	104	1916	104	1916
191.fma3d	2100	161	1307	161	1307
200.sixtrack	1100	202	544	201	548
301.apsi	2600	232	1120	232	1120



Hardware

CPU:	Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 73GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=  -Qprof_gen  PASS2=-Qprof_use
Base tuning for Fortran Programs :  -fast -Qansi_alias +FDO
177.mesa:Base for C Program: -fast +FDO shlw32M.lib
179.art:Base for C Program: -fast +FDO shlw32M.lib
183.equake:Base for C Program: -fast +FDO shlw32M.lib
188.ammp:Base for C Program: -fast +FDO shlw32M.lib
Portability:
178.galgel:   -FI /F32000000
Peak tuning:
168.wupwise:  -fast -Qansi_alias +FDO
171.swim:     -fast -Qansi_alias +FDO
172.mgrid:    -fast -Qansi_alias +FDO
173.applu:  basepeak=yes
177.mesa:   basepeak=yes
178.galgel:   -fast -Qansi_alias +FDO
179.art:      basepeak=yes
183.equake:   -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
```

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Notes/Tuning Information (Continued)

```
187.facerec:  basepeak=yes
188.ammp:     -Oa -QxP
189.lucas:    -fast -Qprefetch- +FDO
191.fma3d:    basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```