# CINT2000 Result

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## Supermicro

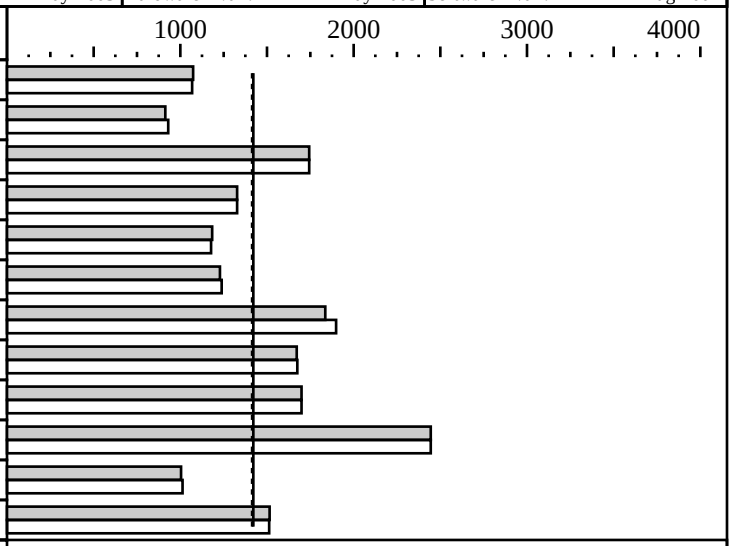
PDSLA/E Motherboard(Intel Pentium D 3.2GHz)

SPECint2000 = 1421

SPECint_base2000 = 1414

SPEC license #01176 | Tested by: Supermicro | Test date: May-2005 | Hardware Avail: May-2005 | Software Avail: Aug-2004

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |
|---|---|---|---|---|---|
| 164.gzip | 1400 | 130 | 1074 | 131 | 1068 |
| 175.vpr | 1400 | 153 | 913 | 151 | 930 |
| 176.gcc | 1100 | 63.1 | 1743 | 63.1 | 1743 |
| 181.mcf | 1800 | 136 | 1328 | 136 | 1328 |
| 186.crafty | 1000 | 84.5 | 1184 | 84.9 | 1177 |
| 197.parser | 1800 | 147 | 1229 | 145 | 1238 |
| 252.eon | 1300 | 70.8 | 1836 | 68.5 | 1899 |
| 253.perlbmk | 1800 | 108 | 1671 | 107 | 1675 |
| 254.gap | 1100 | 64.7 | 1699 | 64.7 | 1699 |
| 255.vortex | 1900 | 77.7 | 2445 | 77.7 | 2445 |
| 256.bzip2 | 1500 | 149 | 1004 | 148 | 1013 |
| 300.twolf | 3000 | 198 | 1515 | 198 | 1513 |




### Hardware

| | |
|---|---|
| CPU: | Intel Pentium D 3.2GHz |
| CPU MHz: | 3200 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1 |
| Parallel: | No |
| Primary Cache: | 12k micro-ops I + 16KBD on chip |
| Secondary Cache: | 1024KB(I+D) on chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 4 X 1GB PC2 5300, Unbuffered, Non-ECC |
| Disk Subsystem: | 1 X 120GB IDE |
| Other Hardware: | N/A |

### Software

| | |
|---|---|
| Operating System: | Windows XP Professional SP2 |
| Compiler: | Intel C++ Compiler 8.1 Build 20040802Z<br>Microsoft Visual Studio .Net 2003(for libraries)<br>SmartHeap Library Version 6.0 from http://www.microquill.com/ |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     -fast -Qansi_alias -Oa  +FDO
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   -fast -Qansi_alias -Oa +FDO
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

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## Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast -O3 +FDO shlW32M.lib
Tested system was built with SC733T-645 chassis and
a 420W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)] is required.
See: http://www.supermicro.com/products/motherboard/DualCore/945/PDSLA.cfm
The system bus runs at 800 MHz
```