CFP2000 Result

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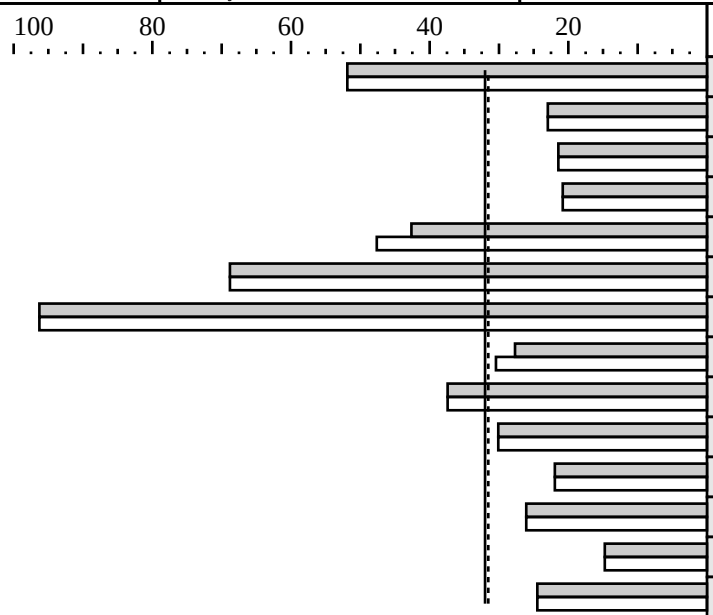
ION

LN20J (3.6GHz Xeon processor w. 2MB L2 cache)

SPECfp_rate2000 = 32.0

SPECfp_rate_base2000 = 31.5

SPEC license #: 2697 | Tested by: ION | Test date: Jul-2005 | Hardware Avail: Apr-2005 | Software Avail: May-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	71.6	51.9	2	71.6	51.9
171.swim	2	313	23.0	2	313	23.0
172.mgrid	2	195	21.4	2	195	21.4
173.applu	2	234	20.8	2	234	20.8
177.mesa	2	76.2	42.6	2	68.2	47.6
178.galgel	2	97.8	68.8	2	97.8	68.8
179.art	2	62.6	96.3	2	62.6	96.3
183.equake	2	109	27.7	2	99.1	30.4
187.facerec	2	118	37.4	2	118	37.4
188.ammp	2	170	30.1	2	170	30.1
189.lucas	2	211	21.9	2	211	21.9
191.fma3d	2	187	26.1	2	187	26.1
200.sixtrack	2	173	14.7	2	173	14.7
301.apsi	2	247	24.5	2	247	24.5

Hardware

CPU:	Intel Xeon processor, 800MHz system bus
CPU MHz:	3591
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1-2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB (I+D) on chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (4x512MB DDR2-400, 3-3-3 DIMMs)
Disk Subsystem:	80GB Seagate SATA disk
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux 4 ES, 2.6.9-5.0.5.ELsmp EM64T
Compiler:	Intel(R) Fortran Compiler for EM64T, V8.1 (Build 20050517) Intel(R) C Compiler for EM64T, V8.1 (Build 20050517)
File System:	ext3
System State:	Multi-user, runlevel 3

Notes/Tuning Information

```
Tested by ION Computer Systems
 Hyper-Threading Technology disabled in BIOS
 Hardware Prefetcher Enabled in BIOS
 Adjacent Cache Line Prefetch Enabled in BIOS
 System BIOS is V8.10 Build 81

 Feedback optimization:
      +FDO:  PASS1 = -prof_gen   PASS2 = -prof_use

 FP Portability Flags:
       178.galgel:  -FI

 FP Baseline optimization flags:
          C programs: -fast -auto_ilp32 -march=pentium4 +FDO
    Fortran programs: -fast +FDO
 Fortran-77 programs: -fast -Zp8 +FDO
```

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Notes/Tuning Information (Continued)

```
FP Peak optimization flags:
     168.wupwise: basepeak=yes
        171.swim: basepeak=yes
        72.mgrid: basepeak=yes
       173.applu: basepeak=yes
        177.mesa: -fast -auto_ilp32 +FDO
      178.galgel: basepeak=yes
         179.art: basepeak=yes
      183.equake: -fast -auto_ilp32 +FDO
     187.facerec: basepeak=yes
        188.ammp: basepeak=yes
       189.lucas: basepeak=yes
       191.fma3d: basepeak=yes
    200.sixtrack: basepeak=yes
        301.apsi: basepeak=yes
```