CINT2000 Result

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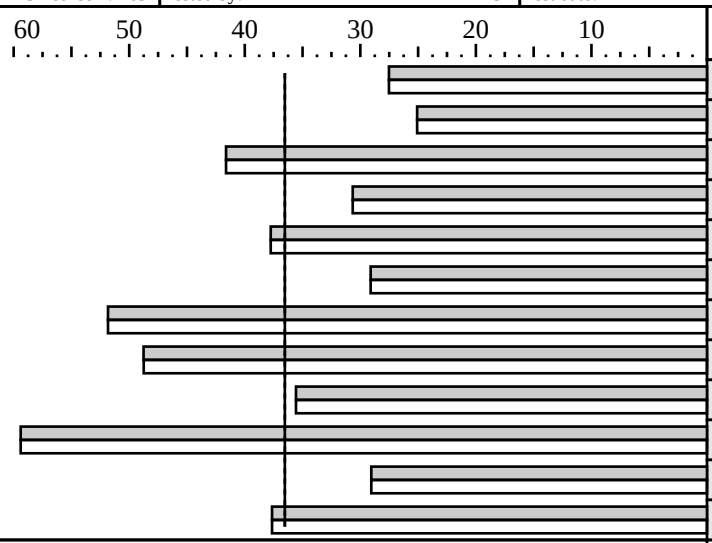
ION

LN20J (3.6GHz Xeon processor w. 2MB L2 cache)

SPECint_rate2000 = 36.5

SPECint_rate_base2000 = 36.5

SPEC license #: 2697 | Tested by: ION | Test date: Jul-2005 | Hardware Avail: Apr-2005 | Software Avail: May-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	118	27.5	2	118	27.5
175.vpr	2	129	25.1	2	129	25.1
176.gcc	2	61.3	41.6	2	61.3	41.6
181.mcf	2	136	30.7	2	136	30.7
186.crafty	2	61.5	37.8	2	61.5	37.8
197.parser	2	143	29.1	2	143	29.1
252.eon	2	58.2	51.8	2	58.2	51.8
253.perlbmk	2	85.6	48.8	2	85.7	48.7
254.gap	2	71.8	35.6	2	71.8	35.6
255.vortex	2	74.2	59.4	2	74.2	59.4
256.bzip2	2	120	29.1	2	120	29.1
300.twolf	2	185	37.6	2	185	37.6

Hardware

CPU:	Intel Xeon processor, 800MHz system bus
CPU MHz:	3591
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1-2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB (I+D) on chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (4x512MB DDR2-400, 3-3-3 DIMMs)
Disk Subsystem:	80GB Seagate SATA disk
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux 4 ES, 2.6.9-5.0.5.ELsmp EM64T
Compiler:	Intel(R) C Compiler for EM64T, V8.1 (Build 20050517) Intel(R) C++ Compiler for EM64T, V8.1 (Build 20050517)
File System:	ext3
System State:	Multi-user, runlevel 3

Notes/Tuning Information

```
Tested by ION Computer Systems
 Hyper-Threading Technology disabled in BIOS
 Hardware Prefetcher Enabled in BIOS
 Adjacent Cache Line Prefetch Enabled in BIOS
 System BIOS is V8.10 Build 81

 Feedback optimization:
      +FDO:  PASS1 = -prof_gen   PASS2 = -prof_use

 INT Portability Flags:
       176.gcc: -Dalloca=_alloca
       176.gcc: srcalt=64bitgcc35
    186.crafty: -DLINUX_i386
       252.eon: -DHAS_ERRLIST
       252.eon: srcalt=fmax_errno
  253.perlbmk: -DSPEC_CPU2000_LINUX_I386 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_GLIBC22 -DSPEC_CPU2000_LP64
       254.gap: -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO
               -DSYS_HAS_SIGNAL_PROTO -DSYS_HAS_ANSI -DSYS_HAS_CALLOC_PROTO
               -DSPEC_CPU2000_LP64
```

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Notes/Tuning Information (Continued)

```
      255.vortex: -DSPEC_CPU2000_LP64
      255.vortex: srcalt=closed_files

   INT Baseline optimization flags:
      C programs: -fast -auto_ilp32 +FDO
    C++ programs: -fast -auto_ilp32 +FDO

   INT Peak optimization flags:
         164.gzip: basepeak=yes
          175.vpr: basepeak=yes
         176.gcc: basepeak=yes
          181.mcf: basepeak=yes
       186.crafty: basepeak=yes
       197.parser: basepeak=yes
          252.eon: basepeak=yes
      253.perlbmk: -fast -Zp8 -auto_ilp32 +FDO
          254.gap: basepeak=yes
       255.vortex: basepeak=yes
        256.bzip2: basepeak=yes
        300.twolf: basepeak=yes
```