CINT2000 Result

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HITACHI

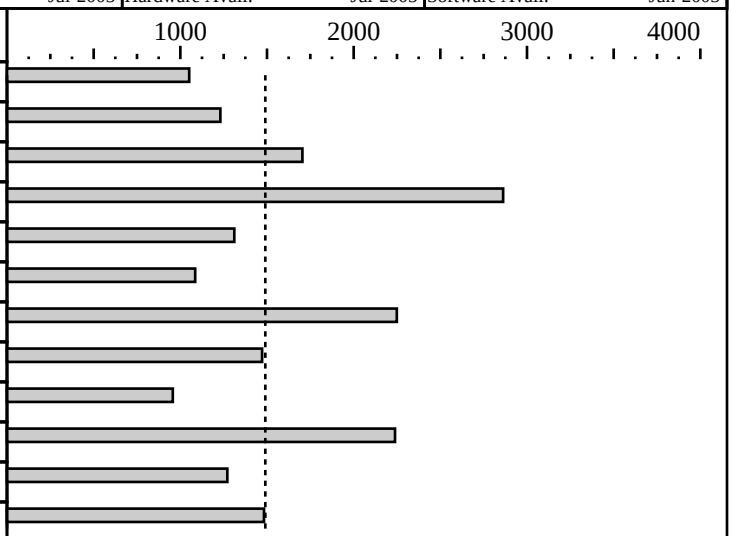
HITACHI BladeSymphony (1.66GHz/9MB Itanium 2)

SPECint2000 = --

SPECint_base2000 = 1490

SPEC license #: 872 | Tested by: HITACHI | Test date: Jul-2005 | Hardware Avail: Jul-2005 | Software Avail: Jun-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	133	1051		
175.vpr	1400	114	1231		
176.gcc	1100	64.5	1704		
181.mcf	1800	62.9	2862		
186.crafty	1000	76.3	1311		
197.parser	1800	166	1086		
252.eon	1300	57.8	2249		
253.perlbmk	1800	122	1472		
254.gap	1100	115	956		
255.vortex	1900	84.9	2239		
256.bzip2	1500	118	1271		
300.twolf	3000	202	1483		



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1666
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	16KBI + 16KBD on chip, per core
Secondary Cache:	256KB(I+D) on chip, per core
L3 Cache:	9.0MB(I+D) on chip, per core
Other Cache:	N/A
Memory:	8GB (1GB DIMM x 8)
Disk Subsystem:	Seagate Ultra320 ST373454LC (73GBx1), 15000rpm
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux AS 3 update 3
Compiler:	Intel(R) C++ Compiler for Linux 9.0 (Build 20050430)
File System:	ext3
System State:	Multi-user run level 3

Notes/Tuning Information

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Base tuning flags:
     C   : -fast -auto_ilp32 +FDO
     C++ : -fast -auto_ilp32 -IPF_fp_relaxed +FDO

+FDO: PASS1=-prof_gen  PASS2=-prof_use

Portability flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_alloca
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST fmax_errno src.alt
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64 closed_files src.alt
  300.twolf: stdfree src.alt

One of two cpus is used.
One is disabled by firmware prior to boot.
```