CFP2000 Result

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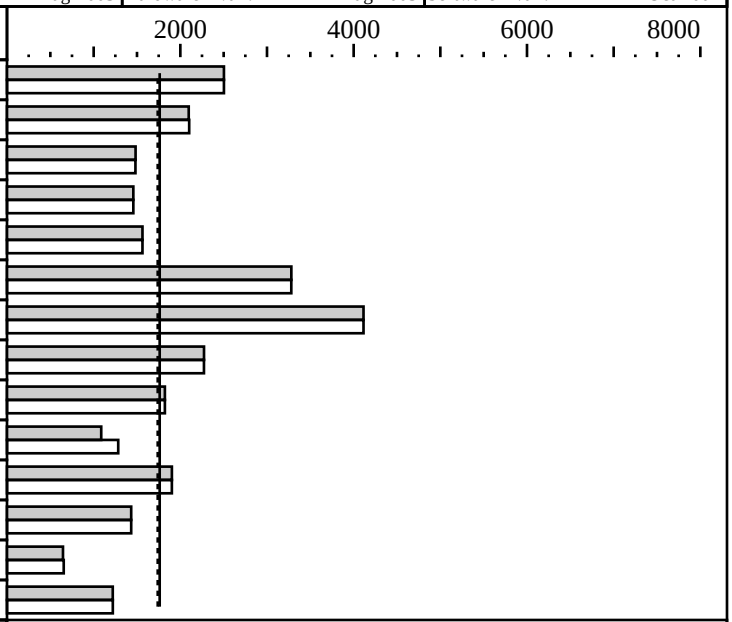
Acer Incorporated
Altos G530 (3.6 GHz Intel Xeon)

SPECfp2000 = 1762

SPECfp_base2000 = 1739

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Aug-2005 | Hardware Avail: Aug-2005 | Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	63.9	2503	63.9	2503
171.swim	3100	148	2097	147	2102
172.mgrid	1800	121	1484	121	1482
173.applu	2100	144	1458	144	1458
177.mesa	1400	89.6	1563	89.6	1563
178.galgel	2900	88.4	3281	88.4	3280
179.art	2600	63.2	4114	63.2	4114
183.equake	1300	57.2	2273	57.2	2273
187.facerec	1900	104	1822	104	1822
188.ammp	2200	202	1087	171	1283
189.lucas	2000	105	1902	105	1902
191.fma3d	2100	147	1433	147	1433
200.sixtrack	1100	170	646	168	655
301.apsi	2600	213	1220	213	1221



Hardware

CPU:	Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4 x 1024MB ECC DDR2-400 SDRAM CL3
Disk Subsystem:	1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790) SP1
Compiler:	Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=  -Qprof_gen  PASS2=-Qprof_use
Base tuning for Fortran Programs :  -fast -Qansi_alias +FDO
177.mesa:Base for C Program: -fast +FDO shlw32M.lib
179.art:Base for C Program: -fast +FDO shlw32M.lib
183.equake:Base for C Program: -fast +FDO shlw32M.lib
188.ammp:Base for C Program: -fast +FDO shlw32M.lib
Portability:
178.galgel:   -FI /F32000000
Peak tuning:
168.wupwise:  -fast -Qansi_alias +FDO
171.swim:     -fast -Qansi_alias +FDO
172.mgrid:    -fast -Qansi_alias +FDO
173.applu:  basepeak=yes
177.mesa:   basepeak=yes
178.galgel:   -fast -Qansi_alias +FDO
179.art:      basepeak=yes
183.equake:   -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
```

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Notes/Tuning Information (Continued)

```
187.facerec:  basepeak=yes
188.ammp:     -Oa -QxP
189.lucas:    -fast -Qprefetch- +FDO
191.fma3d:    basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
```