CINT2000 Result

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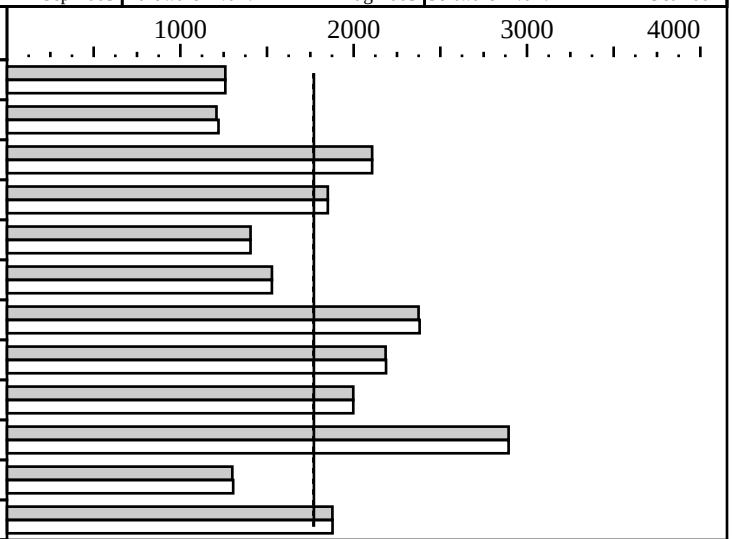
Acer Incorporated
Altos G530 (3.8 GHz Intel Xeon)

SPECint2000 = 1771

SPECint_base2000 = 1768

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Sep-2005 | Hardware Avail: Aug-2005 | Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	111	1260	111	1260
175.vpr	1400	116	1209	115	1220
176.gcc	1100	52.2	2107	52.2	2107
181.mcf	1800	97.3	1851	97.3	1851
186.crafty	1000	71.2	1405	71.2	1405
197.parser	1800	118	1528	118	1528
252.eon	1300	54.8	2374	54.6	2381
253.perlbmk	1800	82.4	2184	82.3	2187
254.gap	1100	55.1	1998	55.1	1998
255.vortex	1900	65.7	2894	65.7	2894
256.bzip2	1500	115	1300	115	1305
300.twolf	3000	160	1877	160	1878



Hardware

CPU:	Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3800
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4 x 1024MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Compiler 8.1 Build 20041019Z for IA32 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

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Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast -O3 +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```