CFP2000 Result

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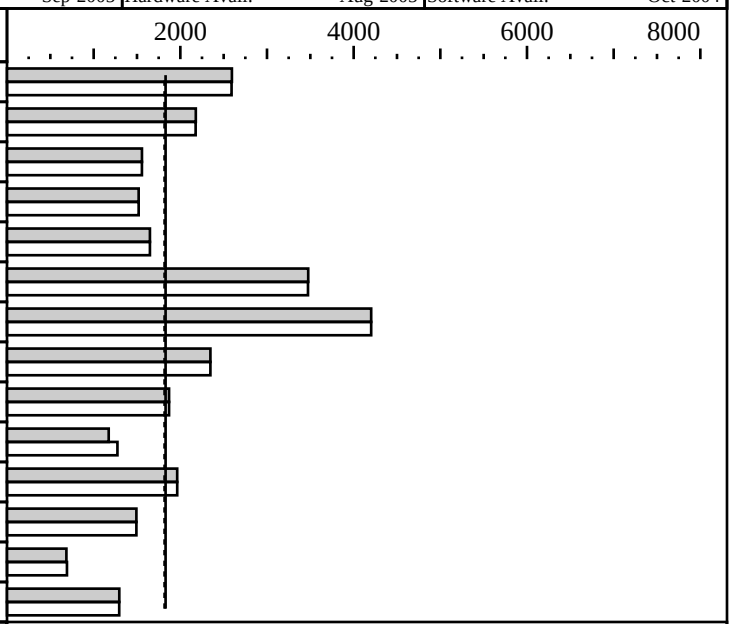
Acer Incorporated
Altos G530 (3.8 GHz Intel Xeon)

SPECfp2000 = 1830

SPECfp_base2000 = 1818

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Sep-2005 | Hardware Avail: Aug-2005 | Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	61.7	2595	61.8	2591
171.swim	3100	142	2179	142	2177
172.mgrid	1800	116	1557	116	1557
173.applu	2100	138	1519	138	1519
177.mesa	1400	84.9	1649	84.9	1649
178.galgel	2900	83.4	3476	83.5	3473
179.art	2600	61.9	4202	61.9	4202
183.equake	1300	55.4	2347	55.4	2347
187.facerec	1900	102	1870	102	1870
188.ammp	2200	187	1174	173	1274
189.lucas	2000	102	1964	102	1964
191.fma3d	2100	141	1493	141	1493
200.sixtrack	1100	161	685	159	692
301.apsi	2600	201	1296	201	1295



Hardware

CPU:	Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3800
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4 x 1024MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Build 20041019Z for IA32 and Fortran Compiler 8.1 Build 20040802Z for IA32 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=  -Qprof_gen  PASS2=-Qprof_use
Base tuning for all Fortran Programs :  -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel:   -FI /F32000000
Peak tuning:
168.wupwise:  -fast -Qansi_alias +FDO
171.swim:     -fast -Qansi_alias +FDO
172.mgrid:    -fast -Qansi_alias +FDO
173.applu:  basepeak=yes
177.mesa:   basepeak=yes
178.galgel:   -fast -Qansi_alias +FDO
179.art:      basepeak=yes
183.equake:   -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec:  basepeak=yes
188.ammp:     -Oa -QxP
189.lucas:    -fast -Qprefetch- +FDO
```

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Notes/Tuning Information (Continued)

```
191.fma3d:    basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```