# CINT2000 Result

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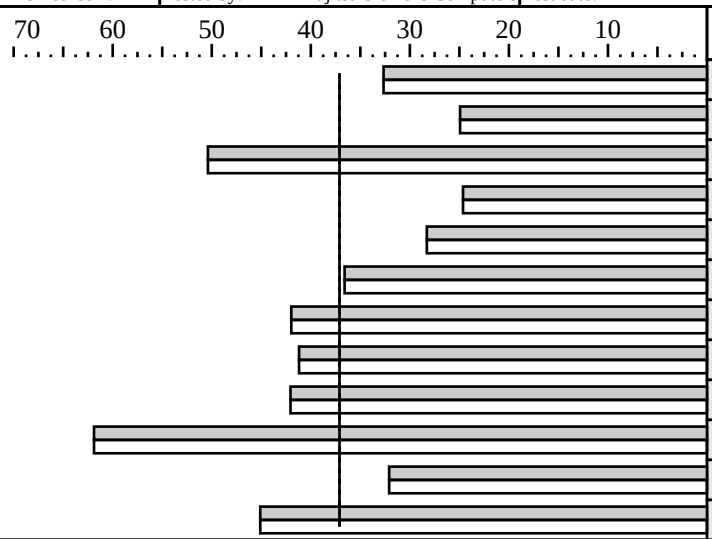
## Fujitsu Siemens Computers

PRIMERGY RX200 S2, Intel Xeon processor 3.20GHz, 2MB L2

SPECint_rate2000 = 37.1

SPECint_rate_base2000 = 37.1

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2005 | Hardware Avail: Oct-2005 | Software Avail: Sep-2005




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 4 | 199 | 32.7 | 4 | 199 | 32.7 |
| 175.vpr | 4 | 261 | 24.9 | 4 | 261 | 24.9 |
| 176.gcc | 4 | 101 | 50.4 | 4 | 101 | 50.4 |
| 181.mcf | 4 | 339 | 24.6 | 4 | 339 | 24.6 |
| 186.crafty | 4 | 164 | 28.3 | 4 | 164 | 28.3 |
| 197.parser | 4 | 228 | 36.6 | 4 | 228 | 36.6 |
| 252.eon | 4 | 144 | 42.0 | 4 | 144 | 42.0 |
| 253.perlbmk | 4 | 203 | 41.2 | 4 | 203 | 41.2 |
| 254.gap | 4 | 121 | 42.1 | 4 | 121 | 42.1 |
| 255.vortex | 4 | 142 | 61.9 | 4 | 142 | 61.9 |
| 256.bzip2 | 4 | 217 | 32.1 | 4 | 217 | 32.1 |
| 300.twolf | 4 | 309 | 45.1 | 4 | 309 | 45.1 |

### Hardware

| | |
|---|---|
| CPU: | Intel Xeon processor 3.20GHz, 2MB L2, 800MHz system bus |
| CPU MHz: | 3200 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology enabled) |
| CPU(s) orderable: | 1,2 |
| Parallel: | No |
| Primary Cache: | 12k micro-ops I + 16KBD on chip, per core |
| Secondary Cache: | 2048KB(I+D) on chip, per core |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 8x512 MB DDRII-RAM PC2-3200R |
| Disk Subsystem: | Seagate ST336753LC(U320 SCSI, 15krpm) |
| Other Hardware: | none |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 Enterprise Edition + SP1 |
| Compiler: | Intel C++ Compiler 9.0<br>Build 20050722Z (for 32-bit applications),<br>Microsoft Visual Studio .Net 2003<br>7.1.3088 (for libraries),<br>SmartHeap Library Version 7.4<br>from http://www.microquill.com |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
GENERAL
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Portability flags
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafy:   -DNT_i386
   252.eon:     Approved srcalt stdcpp is used
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
   for C programs:   -fast +FDO shlW32M.lib
   for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib

Peak tuning flags
   same as baseline (basepeak=true set globally)

New 32-bit Windows tools used, approved in May-2005

The system bus runs at 800 MHz
```

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## Notes/Tuning Information (Continued)

This result was measured with 32-bit binaries using the 32-bit version of the operating system.

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