CINT2000 Result

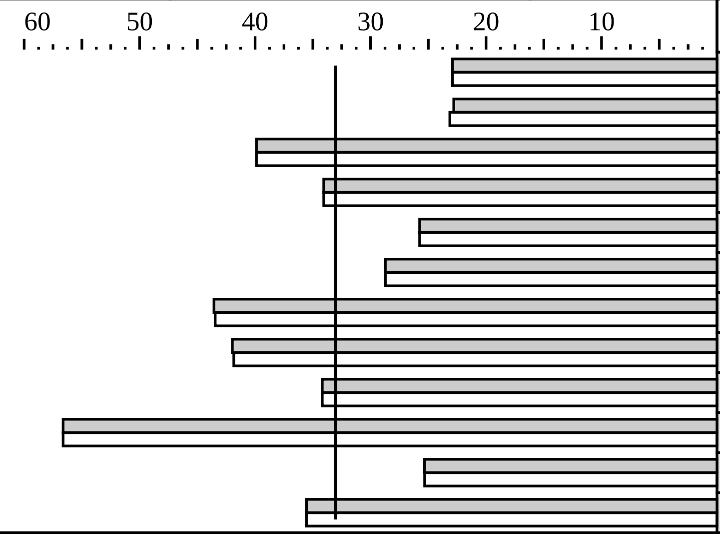
Acer Incorporated

Acer Altos R510 Mk2 (3.0 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECint_rate2000 = 33.0

SPECint_rate_base2000 = 33.0

SPEC license #:	Tested by:	Test date:	Hardware Avail:	Software Avail:
97	Acer Incorporated	Dec-2005	Jan-2006	Apr-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	142	22.9	2	142	22.9
175.vpr	2	142	22.8	2	140	23.1
176.gcc	2	64.0	39.9	2	64.0	39.9
181.mcf	2	123	34.1	2	123	34.1
186.crafty	2	90.1	25.8	2	90.1	25.8
197.parser	2	145	28.7	2	145	28.7
252.eon	2	69.2	43.6	2	69.4	43.5
253.perlbmk	2	99.5	42.0	2	99.8	41.8
254.gap	2	74.7	34.2	2	74.7	34.2
255.vortex	2	77.8	56.6	2	77.8	56.6
256.bzip2	2	137	25.3	2	137	25.3
300.twolf	2	196	35.6	2	196	35.6

Hardware

CPU:	Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology Disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 160GB 7200RPM SATA HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Compiler 9.0 Build 20050430Z for IA32 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

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SPEC license #: 97	Tested by: Acer Incorporated	Test date: Dec-2005	Hardware Avail: Jan-2006	Software Avail: Apr-2005

Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast  +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```