CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

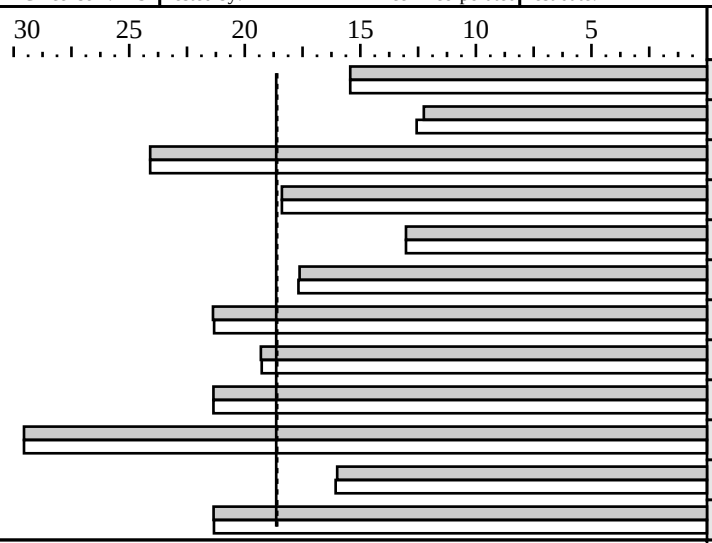
Acer Incorporated

Acer Altos R510 Mk2 (3.0 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECint_rate2000 = 18.6

SPECint_rate_base2000 = 18.6

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Dec-2005 | Hardware Avail: Jan-2006 | Software Avail: Apr-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	210	15.4	2	210	15.4
175.vpr	2	265	12.3	2	259	12.6
176.gcc	2	106	24.1	2	106	24.1
181.mcf	2	227	18.4	2	227	18.4
186.crafty	2	178	13.0	2	178	13.0
197.parser	2	237	17.6	2	236	17.7
252.eon	2	141	21.4	2	141	21.3
253.perlbmk	2	216	19.3	2	217	19.3
254.gap	2	120	21.4	2	120	21.4
255.vortex	2	149	29.6	2	149	29.6
256.bzip2	2	217	16.0	2	217	16.1
300.twolf	2	326	21.3	2	326	21.3

Hardware

CPU:	Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Enabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 160GB 7200RPM SATA HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Compiler 9.0 Build 20050430Z for IA32 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast  +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```