CFP2000 Result

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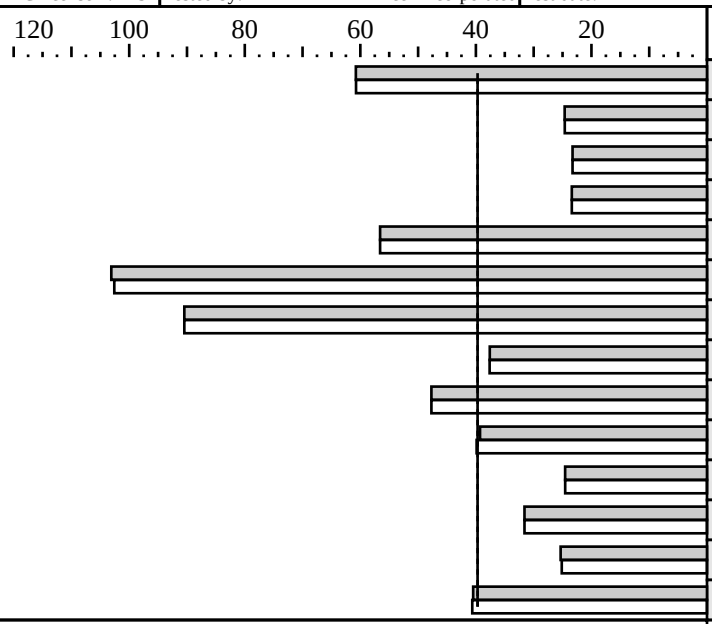
Acer Incorporated

Acer Altos R710 (Dual-Core Intel Xeon 2.8 GHz, 2x2MB L2, 800MHz system bus)

SPECfp_rate2000 = 39.7

SPECfp_rate_base2000 = 39.7

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Dec-2005 | Hardware Avail: Dec-2005 | Software Avail: Jul-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	122	60.8	4	122	60.7
171.swim	4	584	24.6	4	584	24.6
172.mgrid	4	359	23.3	4	359	23.3
173.applu	4	416	23.4	4	416	23.4
177.mesa	4	115	56.6	4	115	56.6
178.galgel	4	131	103	4	131	103
179.art	4	133	90.5	4	133	90.5
183.equake	4	160	37.6	4	160	37.6
187.facerec	4	185	47.7	4	185	47.7
188.ammp	4	260	39.3	4	256	39.9
189.lucas	4	378	24.6	4	378	24.5
191.fma3d	4	308	31.6	4	308	31.6
200.sixtrack	4	201	25.3	4	203	25.1
301.apsi	4	298	40.5	4	297	40.6

Hardware

CPU:	Dual-Core Intel Xeon (2.8 GHz, 2x2MB L2, 800MHz system bus)
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip(Hyper-Threading Technology Disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6 x 1024MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ 9.0 Build 20050430Z for IA32 and Fortran Compiler 9.0 Build 20050722Z for IA32 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=  -Qprof_gen  PASS2=-Qprof_use
Base tuning for all Fortran Programs :  -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel:   -FI /F32000000
192.fma3d:   Approved windowsdp src.alt used
Peak tuning:
168.wupwise:  -fast -Qansi_alias +FDO
171.swim:     -fast -Qansi_alias +FDO
172.mgrid:    -fast -Qansi_alias +FDO
173.applu:  basepeak=yes
177.mesa:   basepeak=yes
178.galgel:   -fast -Qansi_alias +FDO
179.art:      basepeak=yes
183.equake:   -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
187.facerec:  basepeak=yes
188.ammp:     -Oa -QxP
```

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Notes/Tuning Information (Continued)

```
189.lucas:    -fast -Qprefetch- +FDO
191.fma3d:    basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```