CFP2000 Result

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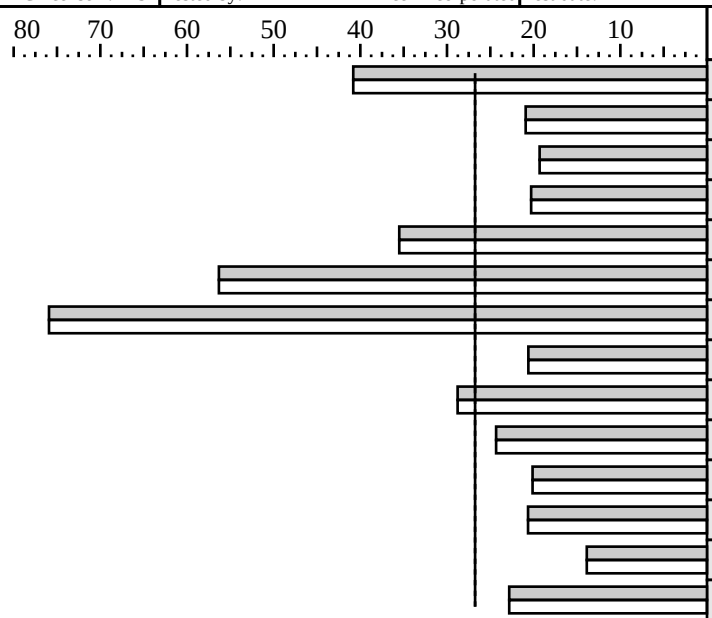
Acer Incorporated

Acer Altos R720 (3.0 GHz Intel Xeon Processor 5050)

SPECfp_rate2000 = 26.8

SPECfp_rate_base2000 = 26.8

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: Jun-2006 | Software Avail: Dec-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	90.9	40.8	2	90.9	40.8
171.swim	2	344	20.9	2	344	20.9
172.mgrid	2	216	19.3	2	216	19.3
173.applu	2	240	20.3	2	240	20.3
177.mesa	2	91.5	35.5	2	91.5	35.5
178.galgel	2	119	56.3	2	119	56.3
179.art	2	79.4	75.9	2	79.4	75.9
183.equake	2	146	20.6	2	146	20.6
187.facerec	2	153	28.8	2	153	28.8
188.ammp	2	210	24.3	2	210	24.3
189.lucas	2	231	20.1	2	231	20.1
191.fma3d	2	236	20.6	2	236	20.6
200.sixtrack	2	184	13.9	2	184	13.9
301.apsi	2	264	22.8	2	264	22.8

Hardware

CPU:	Intel Xeon Processor 5050 (3.0 GHz, 2x2MB L2, 667MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 x 2048MB ECC FB-DIMM 667
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	

Software

Operating System:	Red Hat Enterprise Linux 4 Advanced Server Update 3 EM64T (Kernel 2.6.9-34.ELsmp)
Compiler:	Intel C++ Compiler 9.0 Build 20051201 for EM64T Intel Fortran Compiler 9.0 Build 20051201 for EM64T
File System:	ext3
System State:	Runlevel 3

Notes/Tuning Information

```
GENERAL
 ONESTEP=yes for all benchmarks
 +FDO :  PASS1= -prof_gen   PASS2= -prof_use
PORTABILITY FLAGS
 -DSPEC_CPU2000_LP64 applied to all benchmarks
 178.galgel: -FI for fixed-format Fortran
BASE TUNING
 Baseline optimizations for C and Fortran: -fast +FDO
PEAK TUNING
 basepeak=yes set for all benchmarks
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
```