CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

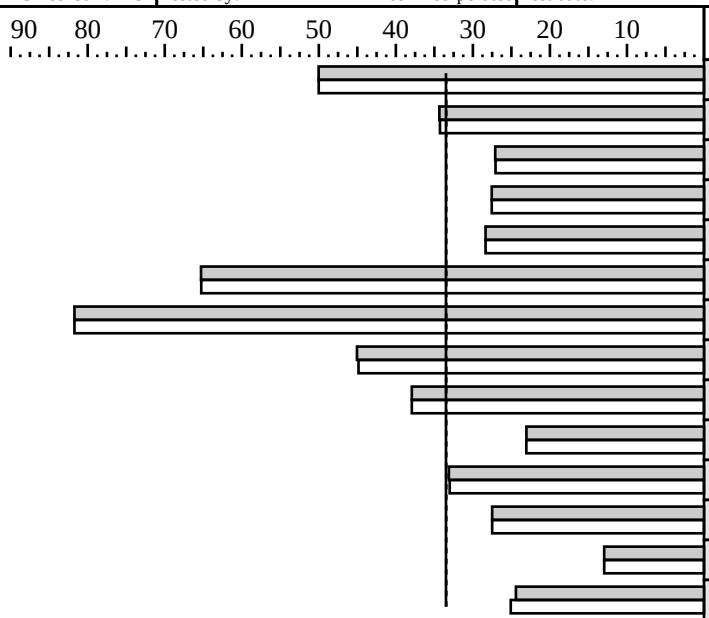
Acer Incorporated

Acer Altos G320 (2.8 GHz Intel Pentium D 920)

SPECfp_rate2000 = 33.5

SPECfp_rate_base2000 = 33.5

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: May-2006 | Software Avail: Nov-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	74.2	50.0	2	74.2	50.0
171.swim	2	209	34.3	2	210	34.3
172.mgrid	2	154	27.1	2	154	27.1
173.applu	2	177	27.6	2	177	27.6
177.mesa	2	115	28.4	2	115	28.4
178.galgel	2	103	65.3	2	103	65.3
179.art	2	73.8	81.7	2	73.8	81.7
183.equake	2	67.0	45.0	2	67.3	44.8
187.facerec	2	116	37.9	2	116	37.9
188.ammp	2	221	23.1	2	221	23.1
189.lucas	2	140	33.1	2	141	33.0
191.fma3d	2	177	27.5	2	177	27.5
200.sixtrack	2	197	13.0	2	197	13.0
301.apsi	2	247	24.4	2	240	25.1

Hardware

CPU:	Intel Pentium D 920 (2.8 GHz, 2x2MB L2, 800MHz system bus)
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4 x 1024MB ECC DDR2-667 SDRAM
Disk Subsystem:	1 x 73GB 10000RPM SATA HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler:	Intel C++ 9.0 Build 20051130Z for IA32 and Fortran Compiler 9.0 Build 20051130Z for IA32 Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=  -Qprof_gen  PASS2=-Qprof_use
Base tuning for all Fortran Programs :  -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel:   -FI /F32000000
Peak tuning:
168.wupwise:  -fast -Qansi_alias +FDO
171.swim:     -fast -Qansi_alias +FDO
172.mgrid:    -fast -Qansi_alias +FDO
173.applu:  basepeak=yes
177.mesa:   basepeak=yes
178.galgel:   -fast -Qansi_alias +FDO
179.art:      basepeak=yes
183.equake:   -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec:  basepeak=yes
188.ammp:     -Oa -QxP
189.lucas:    -fast -Qprefetch- +FDO
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
191.fma3d:    basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi:     -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```