CFP2000 Result

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HCL INFOSYSTEMS LTD

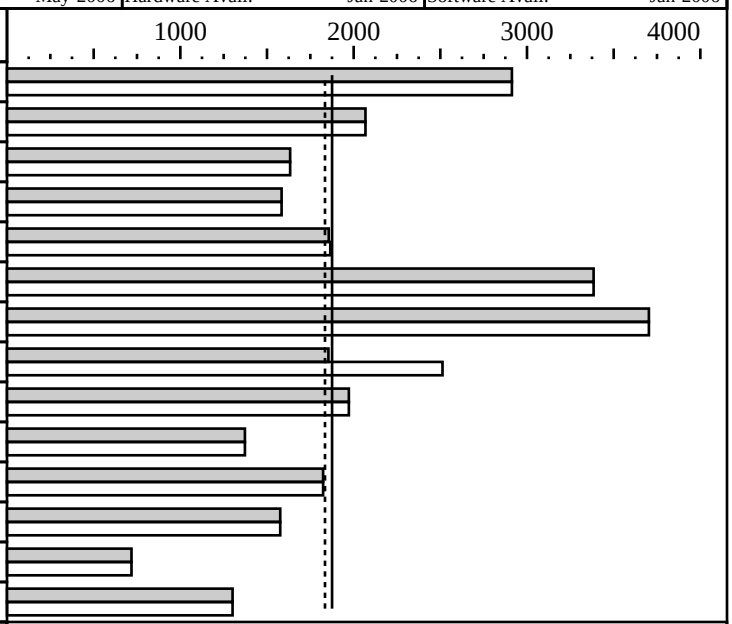
Infiniti Global Line 2700BD2 (3.6GHz Xeon processor with 2MB L2 cache)

SPECfp2000 = 1875

SPECfp_base2000 = 1835

SPEC license #: 2828 | Tested by: HCL INFOSYSTEMS LTD | Test date: May-2006 | Hardware Avail: Jan-2006 | Software Avail: Jan-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	54.9	2913	54.9	2913
171.swim	3100	150	2068	150	2068
172.mgrid	1800	110	1634	110	1634
173.applu	2100	133	1584	133	1584
177.mesa	1400	75.4	1856	75.1	1865
178.galgel	2900	85.7	3384	85.7	3384
179.art	2600	70.2	3703	70.2	3703
183.equake	1300	70.1	1854	51.7	2513
187.facerec	1900	96.3	1972	96.3	1972
188.ammp	2200	160	1373	160	1373
189.lucas	2000	110	1824	110	1824
191.fma3d	2100	133	1576	133	1576
200.sixtrack	1100	153	718	153	718
301.apsi	2600	200	1301	200	1301



Hardware

CPU:	Intel Xeon processor, 800MHz system bus
CPU MHz:	3591
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1-2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 16KB(D) on chip
Secondary Cache:	2048KB (I+D) on chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1024MB DDR2-400)
Disk Subsystem:	73GB Seagate SCSI disk
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux 4 AS U2, 2.6.9-22.EL EM64T
Compiler:	Intel(R) Fortran Compiler for EM64T, V9.0 (Build 20050430) Intel(R) C Compiler for EM64T, V9.0 (Build 20050430)
File System:	ext3
System State:	Multi-user, runlevel 3

Notes/Tuning Information

```
Hyper-Threading Technology Disabled in BIOS
Hardware Prefetcher Enabled in BIOS
Adjacent Cache Line Prefetch Enabled in BIOS

Feedback optimization:
     +FDO:  PASS1 = -prof_gen   PASS2 = -prof_use

FP Portability Flags:
      178.galgel:  -FI

FP Baseline optimization flags:
         C programs: -fast -auto_ilp32 -march=pentium4 +FDO
   Fortran programs: -fast +FDO
Fortran-77 programs: -fast -Zp8 +FDO

FP Peak optimization flags:
     168.wupwise: basepeak=yes
```

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Notes/Tuning Information (Continued)

```
    171.swim: basepeak=yes
     72.mgrid: basepeak=yes
   173.applu: basepeak=yes
    177.mesa: -fast -auto_ilp32 +FDO
  178.galgel: basepeak=yes
     179.art: basepeak=yes
  183.equake: -fast -auto_ilp32 +FDO
 187.facerec: basepeak=yes
    188.ammp: basepeak=yes
   189.lucas: basepeak=yes
   191.fma3d: basepeak=yes
200.sixtrack: basepeak=yes
    301.apsi: basepeak=yes
```