CFP2000 Result

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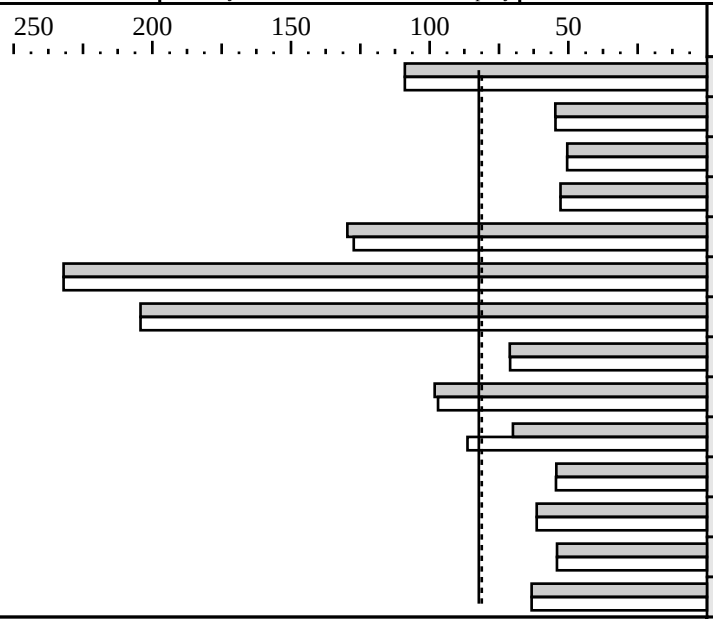
Hewlett-Packard Company

ProLiant BL480c (3.0GHz, Intel Xeon processor 5160)

SPECfp_rate2000 = 82.3

SPECfp_rate_base2000 = 81.2

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jun-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	68.1	109	4	68.1	109
171.swim	4	263	54.7	4	263	54.6
172.mgrid	4	166	50.4	4	166	50.4
173.applu	4	184	52.8	4	184	52.8
177.mesa	4	50.1	130	4	51.0	127
178.galgel	4	58.0	232	4	58.0	232
179.art	4	59.1	204	4	59.1	204
183.equake	4	84.8	71.1	4	85.0	71.0
187.facerec	4	89.8	98.2	4	90.9	97.0
188.ammp	4	146	70.0	4	118	86.4
189.lucas	4	171	54.3	4	170	54.5
191.fma3d	4	159	61.4	4	159	61.4
200.sixtrack	4	94.4	54.1	4	94.3	54.1
301.apsi	4	191	63.2	4	191	63.2

Hardware

CPU:	Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KBI + 32KBD (on chip) per core
Secondary Cache:	4096KB(I+D) (on chip) shared
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1024MB PC2-5300F
Disk Subsystem:	2x36GB 10K SAS
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Intel Fortran Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use


Base tuning for Fortran programs: -fast -Qansi_alias +FDO
Base tuning for C programs:       -fast              +FDO shlw32M.lib
Portability flags:
  178.galgel: -FI /F32000000
Peak tuning:
  168.wupwise:  -fast -Qansi_alias                 +FDO
  171.swim:     -fast                              +FDO ONESTEP=yes
  172.mgrid:    -fast -Qansi_alias                 +FDO
  173.applu:    basepeak=1
  177.mesa:     -fast                              +FDO ONESTEP=yes
  178.galgel:   basepeak=1
  179.art:      basepeak=1
  183.equake:   -fast -Oa -Qrcd                    +FDO ONESTEP=yes shlw32M.lib
  187.facerec:  -fast -Qunroll1 -Qscalar_rep-       +FDO ONESTEP=yes
```

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Notes/Tuning Information (Continued)

```
    188.ammp:     -fast -Qansi_alias                    +FDO
    189.lucas:    -fast
    191.fma3d:    basepeak=1
    200.sixtrack: -fast                                 +FDO
    301.apsi:     basepeak=1
  BIOS Configuration Notes
    Power Regulator set to Static High
    Adjacent Sector Prefetch disabled
```