CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Hewlett-Packard Company

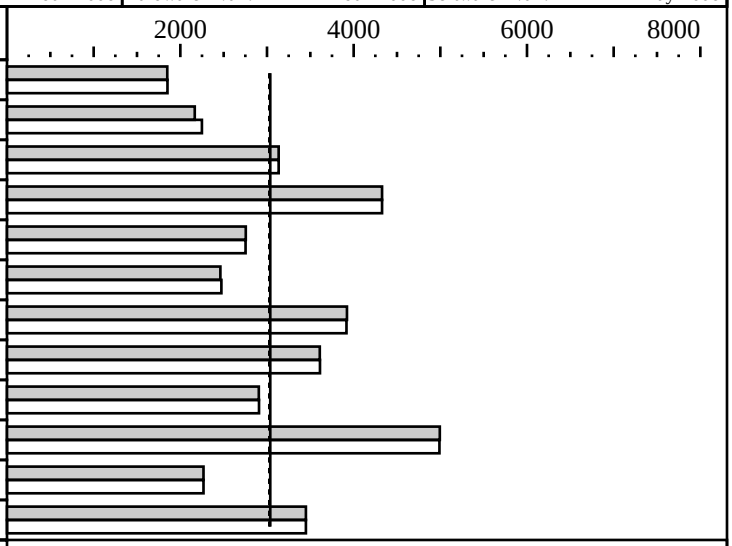
ProLiant BL460c (3.0GHz, Intel Xeon processor 5160)

SPECint2000 = 3038

SPECint_base2000 = 3027

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jun-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	75.7	1849	75.6	1852
175.vpr	1400	64.6	2166	62.3	2249
176.gcc	1100	35.1	3135	35.1	3135
181.mcf	1800	41.6	4328	41.6	4328
186.crafty	1000	36.3	2755	36.3	2753
197.parser	1800	73.1	2462	72.8	2474
252.eon	1300	33.1	3923	33.2	3917
253.perlbmk	1800	49.9	3609	49.8	3611
254.gap	1100	37.9	2906	37.8	2908
255.vortex	1900	38.0	4994	38.1	4990
256.bzip2	1500	66.2	2267	66.2	2267
300.twolf	3000	87.0	3449	87.0	3449



Hardware

CPU:	Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KBI + 32KBD (on chip) per core
Secondary Cache:	4096KB(I+D) (on chip) shared
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1024MB PC2-5300F
Disk Subsystem:	2x36GB 10K SAS
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
  256.bzip2:    basepeak=1
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
  300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
  Power Regulator set to Static High Performance Mode
```