# CINT2000 Result

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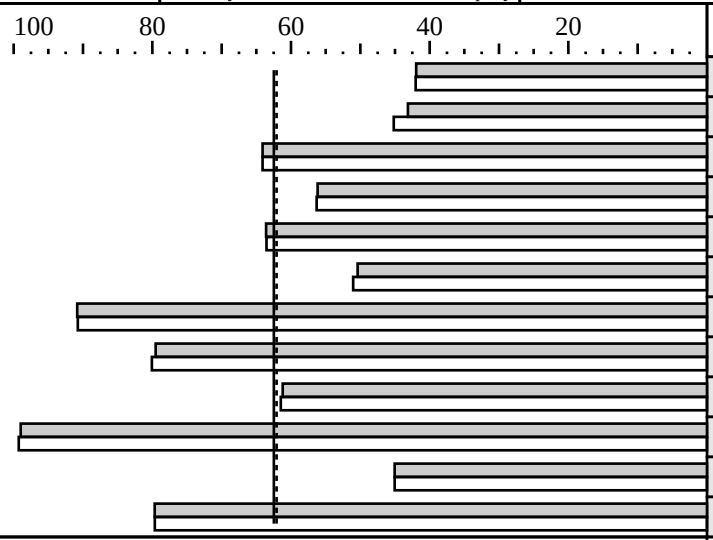
## Hewlett-Packard Company

ProLiant BL460c (3.0GHz, Intel Xeon processor 5160)

SPECint_rate2000 = 62.5

SPECint_rate_base2000 = 62.1

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jun-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 2 | 77.4 | 41.9 | 2 | 77.3 | 42.0 |
| 175.vpr | 2 | 75.3 | 43.1 | 2 | 71.9 | 45.2 |
| 176.gcc | 2 | 39.8 | 64.1 | 2 | 39.8 | 64.1 |
| 181.mcf | 2 | 74.4 | 56.2 | 2 | 74.2 | 56.3 |
| 186.crafty | 2 | 36.5 | 63.6 | 2 | 36.5 | 63.5 |
| 197.parser | 2 | 82.9 | 50.4 | 2 | 81.8 | 51.0 |
| 252.eon | 2 | 33.2 | 90.8 | 2 | 33.2 | 90.7 |
| 253.perlbmk | 2 | 52.5 | 79.5 | 2 | 52.2 | 80.1 |
| 254.gap | 2 | 41.7 | 61.2 | 2 | 41.5 | 61.5 |
| 255.vortex | 2 | 44.5 | 99.0 | 2 | 44.4 | 99.3 |
| 256.bzip2 | 2 | 77.3 | 45.0 | 2 | 77.3 | 45.0 |
| 300.twolf | 2 | 87.4 | 79.7 | 2 | 87.4 | 79.6 |

### Hardware

| | |
|---|---|
| CPU: | Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus) |
| CPU MHz: | 3000 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Parallel: | No |
| Primary Cache: | 32KBI + 32KBD (on chip) per core |
| Secondary Cache: | 4096KB(I+D) (on chip) shared |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 8x1024MB PC2-5300F |
| Disk Subsystem: | 2x36GB 10K SAS |
| Other Hardware: | |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 Enterprise SP1 |
| Compiler: | Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)<br>Microsoft Visual Studio .NET 7.0.9466 (for libraries)<br>MicroQuill Smartheap Library 7.0 |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
  256.bzip2:    basepeak=1
```

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## Notes/Tuning Information (Continued)

```
    300.twolf:    -fast                +FDO               shlw32M.lib
  BIOS Configuration Notes
    Power Regulator set to Static High Performance Mode
    Adjacent Sector Prefetch disabled
```