CFP2000 Result

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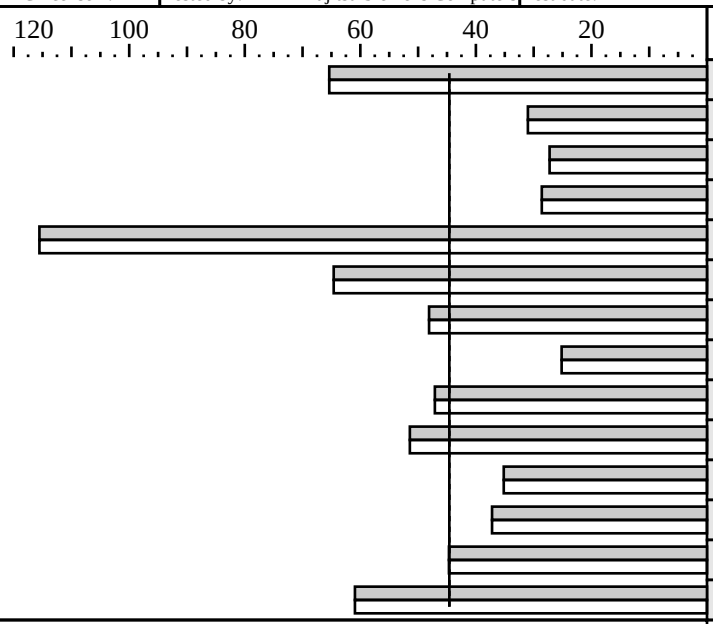
Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7020, 2.66 GHz

SPECfp_rate2000 = 44.6

SPECfp_rate_base2000 = 44.6

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jul-2006 | Hardware Avail: Nov-2005 | Software Avail: Jan-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	227	65.4	8	227	65.4
171.swim	8	928	31.0	8	928	31.0
172.mgrid	8	613	27.2	8	613	27.2
173.applu	8	682	28.6	8	682	28.6
177.mesa	8	112	116	8	112	116
178.galgel	8	417	64.6	8	417	64.6
179.art	8	502	48.1	8	502	48.1
183.equake	8	479	25.2	8	479	25.2
187.facerec	8	374	47.1	8	374	47.1
188.ammp	8	397	51.4	8	397	51.4
189.lucas	8	528	35.2	8	528	35.2
191.fma3d	8	524	37.2	8	524	37.2
200.sixtrack	8	229	44.7	8	229	44.7
301.apsi	8	396	60.9	8	396	60.9

Hardware

CPU:	Intel Xeon processor 7020 (2.66 GHz, 2x1MB L2, 667 MHz system bus)
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2,4
Parallel:	No
Primary Cache:	12k micro-ops I + 16KBD on chip, per core
Secondary Cache:	1024KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)
Disk Subsystem:	Fujitsu MAS3367NC (SCSI, 15krpm, 36GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 9 with SP3 Kernel 2.6.5-7.244-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.0 for EM64T Build 20060120 (for 64-bit applications)
File System:	ext3
System State:	Multi-user run level 3

Notes/Tuning Information

```
GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

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Notes/Tuning Information (Continued)

The system bus runs at 667 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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