CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

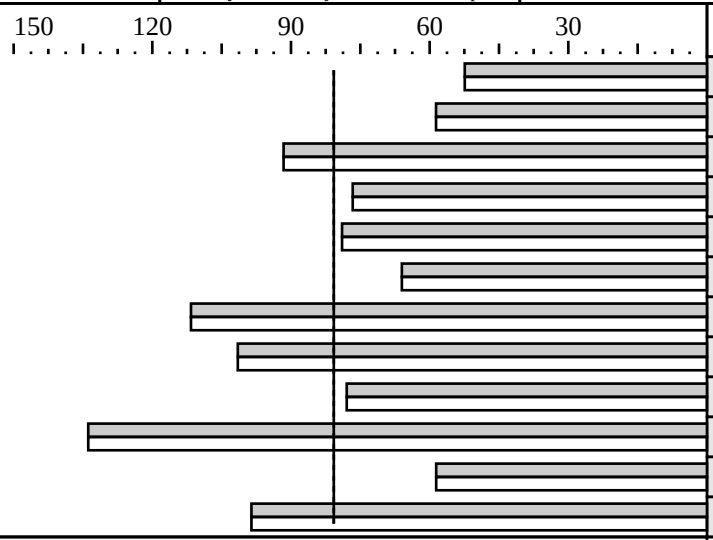
Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon 5120 processor, 1.86 GHz

SPECint_rate2000 = 80.7

SPECint_rate_base2000 = 80.7

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jul-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	124	52.4	4	124	52.4
175.vpr	4	111	58.6	4	111	58.6
176.gcc	4	55.7	91.6	4	55.7	91.6
181.mcf	4	109	76.6	4	109	76.6
186.crafty	4	58.8	79.0	4	58.8	79.0
197.parser	4	127	66.0	4	127	66.0
252.eon	4	54.0	112	4	54.0	112
253.perlbmk	4	82.3	102	4	82.3	102
254.gap	4	65.5	77.9	4	65.5	77.9
255.vortex	4	65.9	134	4	65.9	134
256.bzip2	4	119	58.6	4	119	58.6
300.twolf	4	141	98.6	4	141	98.6

Hardware

CPU:	Intel Xeon 5120 processor (1.86 GHz, 4MB SLC, 1066 MHz system bus)
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	4096KB(I+D) on chip, per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDRII-RAM PC2-4200F (CAS 4-4-4)
Disk Subsystem:	Seagate ST373454SS (SAS, 15.4krpm, 73GB)
Other Hardware:	none

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications), Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries), SmartHeap Library Version 8.0 from http://www.microquill.com
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
GENERAL
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Portability flags
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafy:   -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
   for C programs:   -fast +FDO shlW32M.lib
   for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib

Peak tuning flags
   same as baseline (basepeak=true set globally)

The system bus runs at 1066 MHz

This result was measured with 32-bit binaries using the 32-bit version of
the operating system.
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
BIOS configuration:
Adjacent Sector Prefetch = disable

This result was measured on the PRIMERGY TX300 S3. The PRIMERGY TX300 S3 and
the PRIMERGY RX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```