CINT2000 Result

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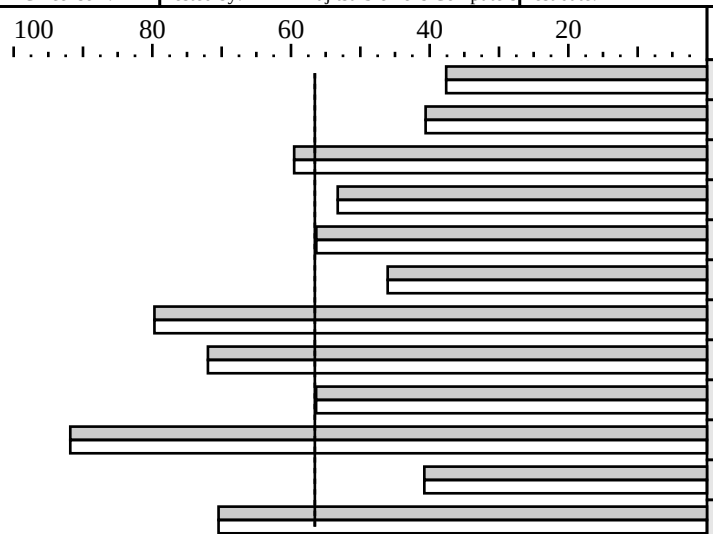
Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon 5150 processor, 2.66 GHz

SPECint_rate2000 = 56.6

SPECint_rate_base2000 = 56.6

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jul-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	86.3	37.6	2	86.3	37.6
175.vpr	2	80.0	40.6	2	80.0	40.6
176.gcc	2	42.9	59.5	2	42.9	59.5
181.mcf	2	78.4	53.3	2	78.4	53.3
186.crafty	2	41.2	56.3	2	41.2	56.3
197.parser	2	90.7	46.1	2	90.7	46.1
252.eon	2	37.8	79.7	2	37.8	79.7
253.perlbmk	2	58.0	72.0	2	58.0	72.0
254.gap	2	45.3	56.4	2	45.3	56.4
255.vortex	2	48.0	91.8	2	48.0	91.8
256.bzip2	2	85.4	40.8	2	85.4	40.8
300.twolf	2	98.8	70.4	2	98.8	70.4

Hardware

CPU:	Intel Xeon 5150 processor (2.66 GHz, 4MB SLC, 1333 MHz system bus)
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	4096KB(I+D) on chip, per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDRII-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem:	Seagate ST373454SS (SAS, 15.4krpm, 73GB)
Other Hardware:	none

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications), Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries), SmartHeap Library Version 8.0 from http://www.microquill.com
File System:	NTFS
System State:	Default

Notes/Tuning Information

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GENERAL
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Portability flags
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafy:   -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
   for C programs:   -fast +FDO shlW32M.lib
   for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib

Peak tuning flags
   same as baseline (basepeak=true set globally)

The system bus runs at 1333 MHz

This result was measured with 32-bit binaries using the 32-bit version of
the operating system.
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Notes/Tuning Information (Continued)

BIOS configuration:
Adjacent Sector Prefetch = disable

This result was measured on the PRIMERGY TX300 S3. The PRIMERGY TX300 S3 and the PRIMERGY RX300 S3 are electronically equivalent.

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