CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

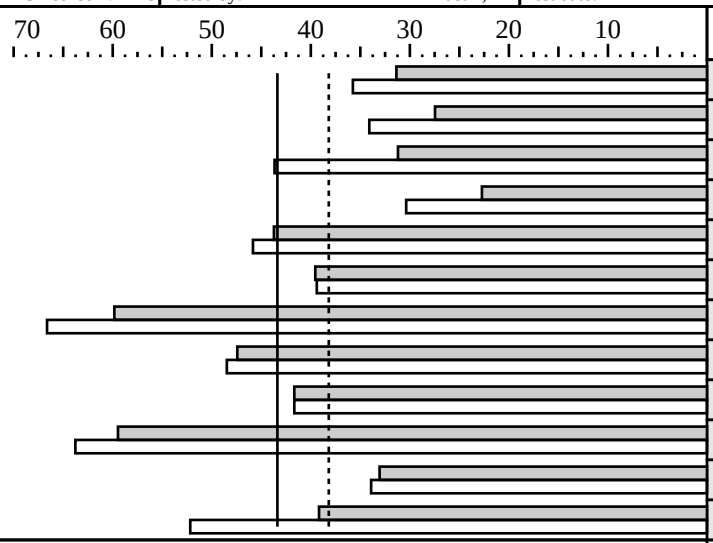
Advanced Micro Devices

ASUS M2N-LR/SATA, AMD Opteron (TM) 1220SE

SPECint_rate2000 = 43.4

SPECint_rate_base2000 = 38.2

SPEC license #: 49 | Tested by: AMD Austin, TX | Test date: Jul-2006 | Hardware Avail: Sep-2006 | Software Avail: Sep-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	104	31.4	2	90.8	35.8
175.vpr	2	118	27.5	2	95.3	34.1
176.gcc	2	81.8	31.2	2	58.5	43.7
181.mcf	2	184	22.7	2	138	30.4
186.crafty	2	53.1	43.7	2	50.6	45.8
197.parser	2	106	39.5	2	106	39.4
252.eon	2	50.4	59.8	2	45.3	66.6
253.perlbmk	2	88.1	47.4	2	86.1	48.5
254.gap	2	61.3	41.7	2	61.3	41.7
255.vortex	2	74.1	59.5	2	69.1	63.8
256.bzip2	2	105	33.1	2	103	33.9
300.twolf	2	178	39.2	2	133	52.2

Hardware

CPU:	AMD Opteron (TM) 1220SE
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1
Parallel:	no
Primary Cache:	64KBI + 64KBD on chip per core
Secondary Cache:	1024KB (I+D) on chip per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB, DDR2-800 CL3
Disk Subsystem:	IDE, 120GB
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise edition SP1 (64-bit)
Compiler:	Intel C++ 9.0 build 20050912Z for IA32 Microsoft Visual Studio .NET 7.0.9466 (libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	default

Notes/Tuning Information

```
shlW32M.lib is the SmartHeap library V7.0 from MicroQuill (www.microquill.com)
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
  176.gcc:      -Dalloca=_alloca /F10000000
  186.crafty:   -DNT_i386
  253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:      -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:   -O3 -Qipo -Op +FDO
Baseline C++: -O3 -Qipo -Qcxx-features +FDO
Peak Tuning:
164.gzip:     -O3 -Qipo -QxW +FDO
175.vpr:      -O3 -Qipo -QxW +FDO
              -Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500
176.gcc:      -O3 -Qipo -QxW -Oi- -Qunroll3 +FDO
181.mcf:      -O3 -Qipo -QaxN      +FDO
186.crafty:   -O3 -Qipo -QxW +FDO
197.parser:   -QxW +FDO -Oi- -Qipo
252.eon:      -O3 -Qipo -QxW +FDO -Qansi_alias
             -Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500
253.perlbmk:  -O3 -Qipo -QxW +FDO
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
 254.gap:       basepeak = yes
 255.vortex:   -O3 -Qipo -arch:SSE  +FDO -Oi- shlW32M.lib
               -Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500
 256.bzip2:    -O3 -Qipo              -Qunroll2
 300.twolf:    -O3 -Qipo -QxW +FDO    -Qunroll3 shlW32M.lib -Qansi_alias
The tested system can be assembled using a standard ATX case and an Antec True 550
watt EPS12V Power Supply.
Half memory slots populated in dual channel configuration.
'start /b /wait /affinity' command is used to bind CPU(s) to processes
```