# CINT2000 Result

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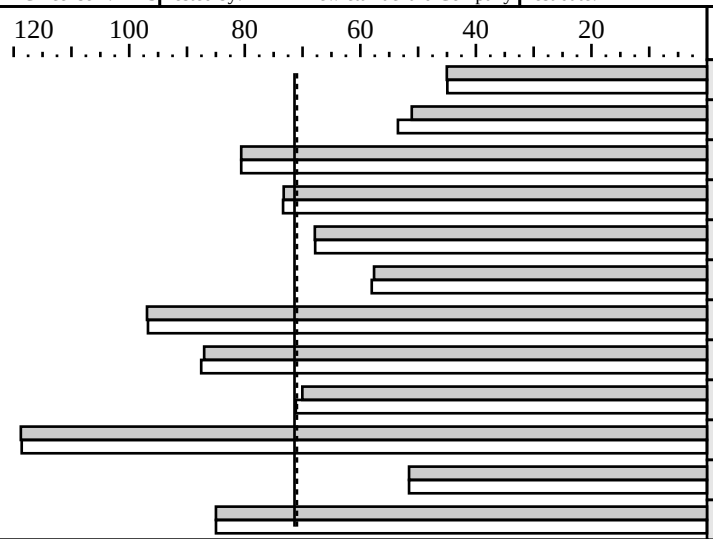
## Hewlett-Packard Company

ProLiant DL380 G5 (1.6 GHz, Intel Xeon processor 5110)

SPECint_rate2000 = 71.4

SPECint_rate_base2000 = 71.0

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 4 | 144 | 45.0 | 4 | 145 | 44.9 |
| 175.vpr | 4 | 127 | 51.1 | 4 | 121 | 53.5 |
| 176.gcc | 4 | 63.3 | 80.6 | 4 | 63.3 | 80.6 |
| 181.mcf | 4 | 114 | 73.3 | 4 | 114 | 73.4 |
| 186.crafty | 4 | 68.4 | 67.9 | 4 | 68.4 | 67.8 |
| 197.parser | 4 | 145 | 57.6 | 4 | 144 | 58.0 |
| 252.eon | 4 | 62.2 | 96.9 | 4 | 62.3 | 96.8 |
| 253.perlbmk | 4 | 96.0 | 87.0 | 4 | 95.4 | 87.5 |
| 254.gap | 4 | 72.9 | 70.0 | 4 | 71.7 | 71.2 |
| 255.vortex | 4 | 74.2 | 119 | 4 | 74.3 | 119 |
| 256.bzip2 | 4 | 135 | 51.6 | 4 | 135 | 51.6 |
| 300.twolf | 4 | 164 | 85.0 | 4 | 164 | 85.0 |

### Hardware

| | |
|---|---|
| CPU: | Intel Xeon processor 5110 (1.6 GHz, 4 MB L2 shared, 1066 MHz bus) |
| CPU MHz: | 1600 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Parallel: | No |
| Primary Cache: | 32KB I + 32 KB D on chip per core |
| Secondary Cache: | 4 MB I+D on chip per chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 16 GB (8x2048 MB PC2-5300F) |
| Disk Subsystem: | 1x36 GB 10 K SAS |
| Other Hardware: | |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 Enterprise SP1 |
| Compiler: | Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)<br>Microsoft Visual Studio .NET 7.0.9466 (for libraries)<br>MicroQuill Smartheap Library 7.0 |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
```

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## Notes/Tuning Information (Continued)

```
    256.bzip2:    basepeak=1
    300.twolf:    -fast               +FDO              shlw32M.lib
  BIOS Configuration Notes
    Power Regulator set to Static High Performance Mode
    Adjacent Sector Prefetch disabled
```