CFP2000 Result

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ION

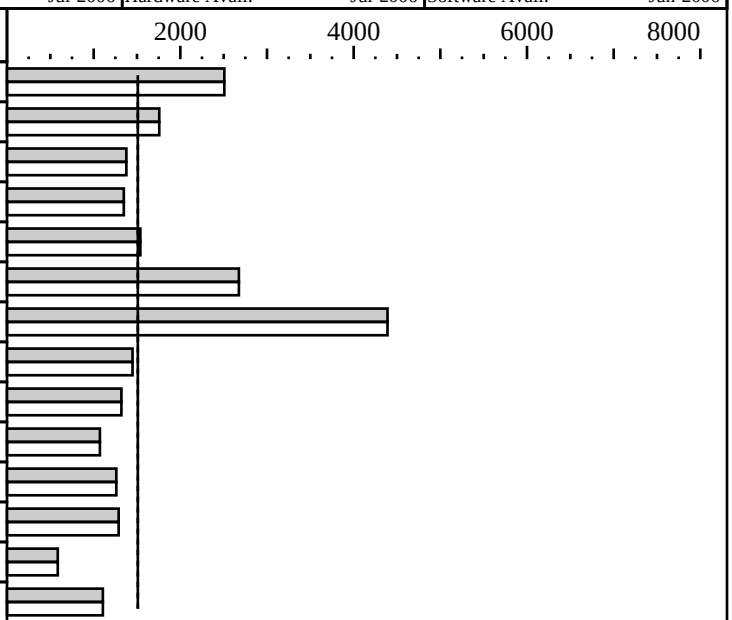
A20 (Intel Xeon processor 5050, 3.0GHz)

SPECfp2000 = 1510

SPECfp_base2000 = 1510

SPEC license #: 2697 | Tested by: ION | Test date: Jul-2006 | Hardware Avail: Jul-2006 | Software Avail: Jun-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	63.8	2508	63.8	2508
171.swim	3100	176	1757	176	1757
172.mgrid	1800	131	1377	131	1377
173.applu	2100	156	1348	156	1348
177.mesa	1400	91.0	1539	91.0	1539
178.galgel	2900	108	2676	108	2676
179.art	2600	59.2	4390	59.2	4390
183.equake	1300	89.7	1449	89.7	1449
187.facerec	1900	144	1320	144	1320
188.ammp	2200	205	1072	205	1072
189.lucas	2000	159	1261	159	1261
191.fma3d	2100	163	1288	163	1288
200.sixtrack	1100	188	586	188	586
301.apsi	2600	235	1106	235	1106



Hardware

CPU:	Intel Xeon Processor 5050 (667Mhz System Bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip (HyperThreading Technology disabled)
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	12KB(I) + 16KB(D) on chip, per core
Secondary Cache:	2MB (I+D) on chip, per core
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8*1GB PC2-5300 FB-DIMMs)
Disk Subsystem:	1 x 400GB SATA, 7200rpm
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux ES 4 Update 3 for AMD64/Intel EM64T
Compiler:	Intel(R) Fortran Compiler for EM64T, V9.1 (Build 20060519) Intel(R) C++ Compiler for EM64T, V9.1 (Build 20060519)
File System:	ext3
System State:	Multi-user

Notes/Tuning Information

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Tested by ION Computer Systems
 BIOS 2.00 Build 54
   HyperThreading Technology Disabled in BIOS
   Enhanced SpeedStep Disabled in BIOS
 kernel 2.6.9-34.0.1.ELsmp
 +FDO: PASS1=-prof_gen  PASS2=-prof_use

    SPEC CFP2000 Baseline optimization flags
          C programs: -fast -ansi_alias +FDO
    Fortran programs: -fast +FDO
 +FDO: PASS1=-prof_gen  PASS2=-prof_use

 SPEC CFP2000 Peak optimization flags:
      basepeak=yes  set globally

 Portability Flags:
   178.galgel:  -FI
```