CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

WIPRO LIMITED

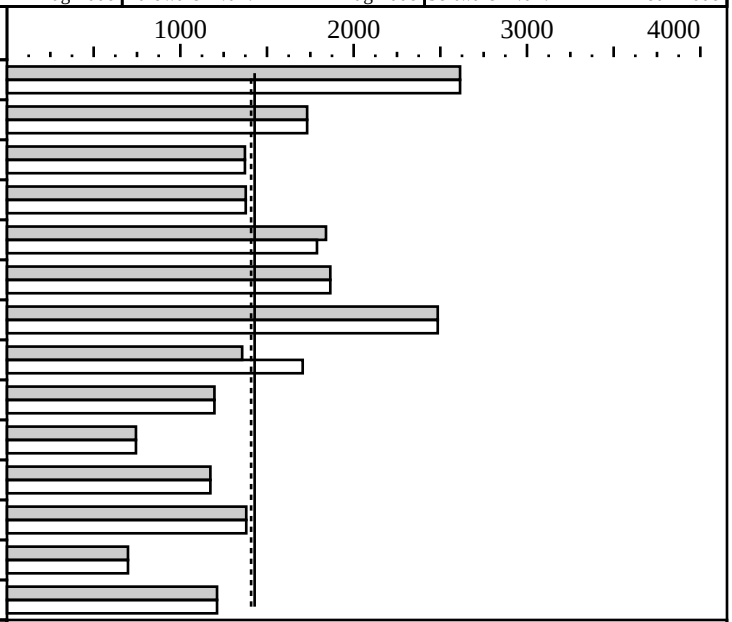
NP9426N4UCR(3.66GHz Xeon processor 1MB L2 cache)

SPECfp2000 = 1429

SPECfp_base2000 = 1408

SPEC license #: 937 | Tested by: WIPRO LIMITED | Test date: Aug-2006 | Hardware Avail: Aug-2006 | Software Avail: Jun-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	61.2	2614	61.2	2614
171.swim	3100	179	1732	179	1732
172.mgrid	1800	131	1373	131	1373
173.applu	2100	152	1377	152	1377
177.mesa	1400	76.1	1840	78.3	1788
178.galgel	2900	155	1865	155	1865
179.art	2600	105	2485	105	2485
183.equake	1300	95.8	1357	76.2	1706
187.facerec	1900	159	1197	159	1197
188.ammp	2200	296	744	296	744
189.lucas	2000	170	1173	170	1173
191.fma3d	2100	152	1380	152	1380
200.sixtrack	1100	158	698	158	698
301.apsi	2600	215	1212	215	1212



Hardware

CPU:	Intel Xeon processor, 800MHz system bus
CPU MHz:	3657
FPU:	Integrated
CPU(s) enabled:	4 cores, 4 chips, 1 core/chip, HyperThreading Disabled
CPU(s) orderable:	1-4
Parallel:	No
Primary Cache:	12K(I) micro-ops + 16KB(D) on chip (per chip)
Secondary Cache:	1024KB (I+D) on chip (per chip)
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (16x1024MB DDR2-400)
Disk Subsystem:	36GB Maxtor SCSI disk
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux 4 AS , 2.6.9-22.ELsmp EM64T
Compiler:	Intel(R) Fortran Compiler for EM64T, V9.1 (Build 20060323) Intel(R) C Compiler for EM64T, V9.1 (Build 20030323)
File System:	ext3
System State:	Multi-user, runlevel 3

Notes/Tuning Information

```
HyperThreading Disabled in BIOS
Hardware Prefetcher Enabled in BIOS
Adjacent Cache Line Prefetch Enabled in BIOS

Feedback optimization:
     +FDO:  PASS1 = -prof_gen   PASS2 = -prof_use

FP Portability Flags:
      178.galgel:  -FI

FP Baseline optimization flags:
         C programs: -fast -auto_ilp32 -march=pentium4 +FDO
   Fortran programs: -fast +FDO

FP Peak optimization flags:
     168.wupwise: basepeak=yes
        171.swim: basepeak=yes
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
    72.mgrid: basepeak=yes
   173.applu: basepeak=yes
    177.mesa: -fast -auto_ilp32 +FDO
  178.galgel: basepeak=yes
     179.art: basepeak=yes
  183.equake: -fast -auto_ilp32 +FDO
 187.facerec: basepeak=yes
    188.ammp: basepeak=yes
   189.lucas: basepeak=yes
   191.fma3d: basepeak=yes
200.sixtrack: basepeak=yes
    301.apsi: basepeak=yes
```