CFP2000 Result

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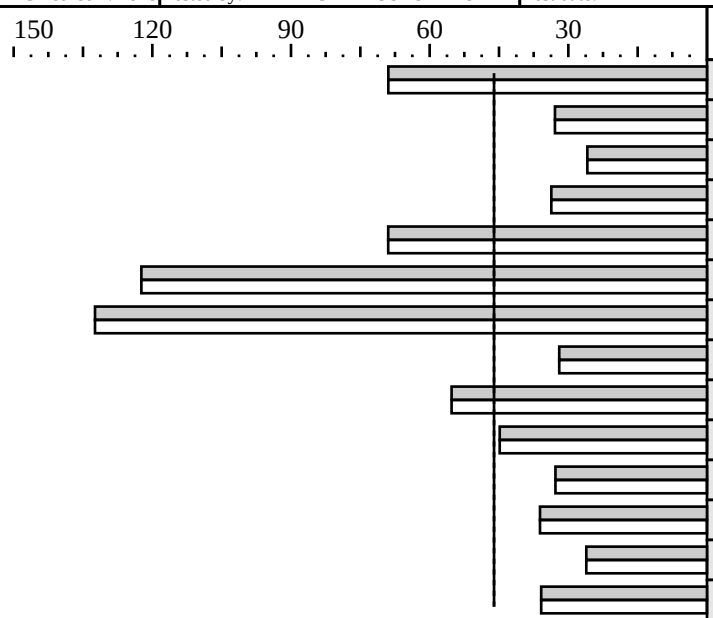
HCL INFOSYSTEMS LTD

Infiniti Global Line/NM 2700AO (3.0 GHz Intel Xeon Processor 5160)

SPECfp_rate2000 = 46.1

SPECfp_rate_base2000 = 46.1

SPEC license #: 2828 | Tested by: HCL INFOSYSTEMS LTD | Test date: Aug-2006 | Hardware Avail: Aug-2006 | Software Avail: Aug-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	53.8	68.9	2	53.8	68.9
171.swim	2	219	32.9	2	219	32.9
172.mgrid	2	161	25.9	2	161	25.9
173.applu	2	145	33.7	2	145	33.7
177.mesa	2	47.1	69.0	2	47.1	69.0
178.galgel	2	55.0	122	2	55.0	122
179.art	2	45.6	132	2	45.6	132
183.equake	2	94.3	32.0	2	94.3	32.0
187.facerec	2	79.8	55.3	2	79.8	55.3
188.ammp	2	114	44.8	2	114	44.8
189.lucas	2	142	32.8	2	142	32.8
191.fma3d	2	135	36.1	2	135	36.1
200.sixtrack	2	97.7	26.1	2	97.7	26.1
301.apsi	2	168	35.9	2	168	35.9

Hardware

CPU:	Intel Xeon 5160 processor (3.0 GHz, 4MB L2, 1333 MHz system bus)
CPU MHz:	2992
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KBI + 32KBD on chip, per core
Secondary Cache:	4096KB(I+D) on chip, per chip
L3 Cache:	None
Other Cache:	None
Memory:	8x1024 GB ECC FB-DIMM DDR2-667 CL5
Disk Subsystem:	Seagate ST3802110AS (SATA, 7.2krpm, 80GB)
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux 4 AS U3, 2.6.9-34.ELsmp EM64T
Compiler:	Intel(R) Fortran Compiler for EM64T, V9.1 (Build 20060707) Intel(R) C++ Compiler for EM64T, V9.1 (Build 20060707)
File System:	ext3
System State:	Multi-user, runlevel 3

Notes/Tuning Information

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  Enhanced SpeedStep Disabled in BIOS
  Hardware Prefetcher Enabled in BIOS (Default)
  Adjacent Cache Line Prefetch Enabled in BIOS (Default)
  2.6.9-34.ELsmp
+FDO: PASS1=-prof_gen  PASS2=-prof_use

   SPEC CFP2000 Baseline optimization flags
        C programs: -fast -ansi_alias +FDO
   Fortran programs: -fast +FDO

SPEC CFP2000 Peak optimization flags:
     basepeak=yes  set globally

Portability Flags:
  178.galgel:  -FI
```