CINT2000 Result

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ASUS Computer International

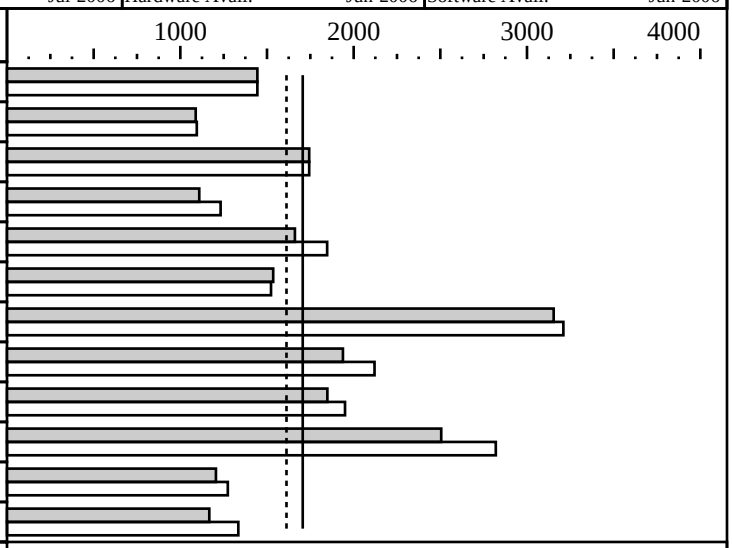
Asus M2N32-SLI Deluxe, AMD Athlon (TM) 64 5000+

SPECint2000 = 1706

SPECint_base2000 = 1612

SPEC license #: 13 | Tested by: Intel Corporation | Test date: Jul-2006 | Hardware Avail: Jun-2006 | Software Avail: Jun-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	96.9	1444	96.9	1444
175.vpr	1400	129	1089	128	1095
176.gcc	1100	63.1	1743	63.1	1743
181.mcf	1800	162	1109	146	1232
186.crafty	1000	60.2	1661	54.1	1847
197.parser	1800	117	1536	118	1523
252.eon	1300	41.2	3154	40.5	3210
253.perlbmk	1800	92.9	1938	84.9	2120
254.gap	1100	59.5	1848	56.4	1950
255.vortex	1900	75.8	2505	67.4	2820
256.bzip2	1500	124	1205	118	1274
300.twolf	3000	257	1167	225	1335



Hardware

CPU:	AMD Athlon (TM) 64 5000+
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1
Parallel:	no
Primary Cache:	64KBI + 64KBD on chip, per core
Secondary Cache:	512KB (I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1 GB, Corsair CM2X1024-8500C5 DDR2-1066 (manually set to DDR2-800 5-5-5-15 in the bios)
Disk Subsystem:	SATA, Western Digital WD740GD, 10000 rpm
Other Hardware:	None

Software

Operating System:	Microsoft Windows XP SP2
Compiler:	Intel C++ 9.1 build 20060519Z for IA32 Microsoft Visual Studio .NET 7.0.9466 (libraries) MicroQuill Smartheap Library 8.0
File System:	NTFS
System State:	default

Notes/Tuning Information

```
GENERAL
+FDO: PASS1= -Qprof_gen  PASS2= -Qprof_use
 shlW32M-80.lib is the SmartHeap library V8.0 from MicroQuill www.microquill.com

 +FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
 Portability:
   176.gcc:      -Dalloca=_alloca /F10000000
   186.crafty:   -DNT_i386
   253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:      -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
   for C programs:   -fast -arch:SSE2 -Oi- +FDO
   for C++ programs: -fast -arch:SSE2 -Qcxx-features +FDO

   164.gzip:    basepeak=yes
   175.vpr:     -O3 -Qipo -QxW -Qoption,c,-ip_ninl_max_stats=2000,
                -ip_ninl_max_total_stats=4500 +FDO
   176.gcc:     basepeak=yes
   181.mcf:     -O3 -Qipo -QaxN +FDO shlW32M-80.lib
```

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Notes/Tuning Information (Continued)

```
   186.crafty:  -O3 -Qipo -QxW +FDO shlW32M-80.lib
   197.parser:  -Oi -Qipo -QxW +FDO shlW32M-80.lib
   252.eon:     -O3 -Qipo -QxW -Qansi_alias -Qoption,c,-ip_ninl_max_stats=2000,
                -ip_ninl_max_total_stats=4500 +FDO shlW32M-80.lib
   253.perlbmk: -O3 -Qipo -QxW +FDO shlW32M-80.lib
   254.gap:     -O3 -Qipo -QxW +FDO shlW32M-80.lib
   255.vortex:  -O3 -Qipo -arch:SSE -Oi- -Qoption,c,-ip_ninl_max_stats=2000,
                -ip_ninl_max_total_stats=4500 +FDO shlW32M-80.lib
The SUT was built with a discrete PCIe graphics card (ATI Radeon X850 XT)
and an ATX 550W power supply. The 2 DIMMs were configured in dual-channel mode.
```