CINT2000 Result

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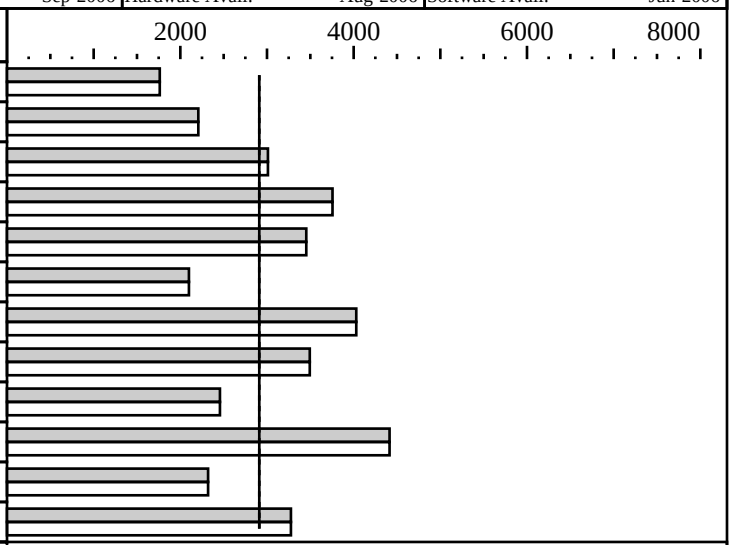
Rackable Systems
C1000-L03-25W30 (Intel Xeon 5160)

SPECint2000 = 2911

SPECint_base2000 = 2911

SPEC license #: 64 | Tested by: Rackable Systems | Test date: Sep-2006 | Hardware Avail: Aug-2006 | Software Avail: Jun-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	79.4	1763	79.4	1763
175.vpr	1400	63.4	2208	63.4	2208
176.gcc	1100	36.5	3011	36.5	3011
181.mcf	1800	47.9	3756	47.9	3756
186.crafty	1000	29.0	3453	29.0	3453
197.parser	1800	85.7	2099	85.7	2099
252.eon	1300	32.3	4030	32.3	4030
253.perlbmk	1800	51.5	3493	51.5	3493
254.gap	1100	44.8	2457	44.8	2457
255.vortex	1900	43.0	4414	43.0	4414
256.bzip2	1500	64.6	2320	64.6	2320
300.twolf	3000	91.6	3276	91.6	3276



Hardware

CPU:	Intel(R) Xeon(R) CPU 5160 @ 3.0GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chip(s)
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	4096KB(I+D) on chip, per chip, shared
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4 x 1024MB ECC FB-DIMM DDR2-667MHz
Disk Subsystem:	1 x 250GB SATA HDD
Other Hardware:	

Software

Operating System:	Red Hat Enterprise Linux 4 Update 3 EM64T
Compiler:	Intel C++ Compiler 9.1.042 for EM64T
File System:	ext3
System State:	Runlevel 3

Notes/Tuning Information

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186.crafty: -DLINUX_i386 -DSPEC_CPU2000_LP64
252.eon: -DHAS_ERRLIST -DSPEC_CPU2000_LP64
253.perlbmk: -DSPEC_CPU2000_LINUX_I386 -DSPEC_CPU2000_NEED_BOOL -DSPEC_CPU2000_LP64
254.gap: -DSYS_IS_USG -DSYS_HAS_CALLOC_PROTO -DSYS_HASMALLOC_PROTO
         -DSYS_HAS_IOCTL_PROTO -DSPEC_CPU2000_LP64
255.vortex: -DSPEC_CPU2000_LP64
Portability for integer benchmarks
Optimization flags
ONESTEP=yes for all benchmarks
+FDO implies feedback-directed optimization PASS1: -prof_gen   PAS2: -prof_use
Baseline optimizations for C:   -fast -auto_ilp32 +FDO
Baseline optimizations for C++: -fast -auto_ilp32 +FDO
basepeak=yes set for all benchmarks
Taskset utility used to bind process to CPU(s)
```