CINT2000 Result

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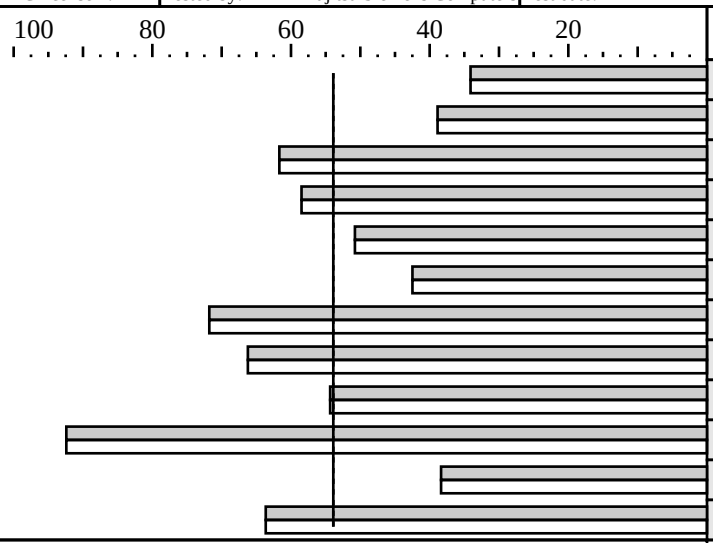
Fujitsu Siemens Computers

PRIMERGY TX150 S5, Intel Xeon processor 3060, 2.40 GHz

SPECint_rate2000 = 53.9

SPECint_rate_base2000 = 53.9

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Aug-2006 | Hardware Avail: Sep-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	95.2	34.1	2	95.2	34.1
175.vpr	2	83.6	38.9	2	83.6	38.9
176.gcc	2	41.4	61.7	2	41.4	61.7
181.mcf	2	71.4	58.5	2	71.4	58.5
186.crafty	2	45.7	50.8	2	45.7	50.8
197.parser	2	98.3	42.5	2	98.3	42.5
252.eon	2	42.0	71.8	2	42.0	71.8
253.perlbmk	2	63.1	66.2	2	63.1	66.2
254.gap	2	47.0	54.4	2	47.0	54.4
255.vortex	2	47.7	92.4	2	47.7	92.4
256.bzip2	2	90.7	38.4	2	90.7	38.4
300.twolf	2	109	63.6	2	109	63.6

Hardware

CPU:	Intel Xeon processor 3060 (2.40 GHz, 4MB L2, 1066 MHz system bus)
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	4096KB(I+D) on chip, per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x2GB DDRII-RAM PC2-4200E (CAS 4-4-4)
Disk Subsystem:	Western Digital (SATA, 7.2krpm, 160GB)
Other Hardware:	none

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications), Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries), SmartHeap Library Version 8.0 from http://www.microquill.com
File System:	NTFS
System State:	Default

Notes/Tuning Information

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GENERAL
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Portability flags
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafty:  -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
   for C programs:   -fast +FDO shlW32M.lib
   for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib

Peak tuning flags
   same as baseline (basepeak=true set globally)

The system bus runs at 1066 MHz

This result was measured with 32-bit binaries using the 32-bit version of
the operating system.
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Notes/Tuning Information (Continued)

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BIOS configuration:
Adjacent Sector Prefetch = disable

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