# CINT2000 Result

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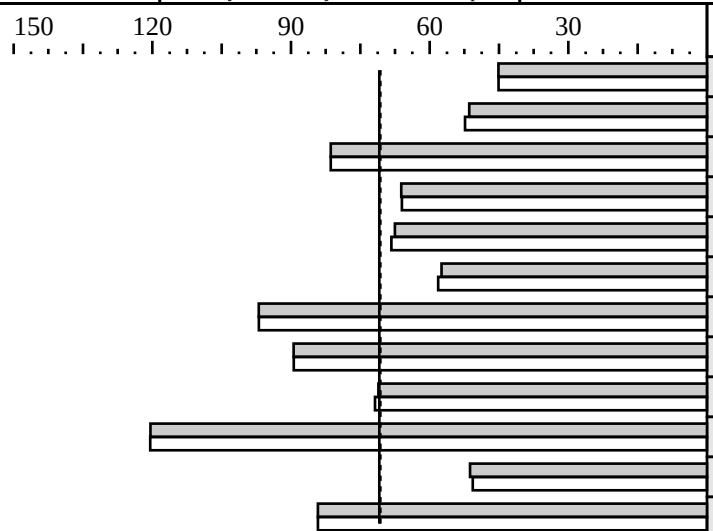
## Fujitsu Siemens Computers
## CELSIUS R540, Intel Xeon 5110 processor

SPECint_rate2000 = 70.9

SPECint_rate_base2000 = 70.7

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Sep-2006 | Hardware Avail: Aug-2006 | Software Avail: May-2006




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 4 | 144 | 45.1 | 4 | 144 | 45.1 |
| 175.vpr | 4 | 126 | 51.4 | 4 | 124 | 52.3 |
| 176.gcc | 4 | 62.7 | 81.4 | 4 | 62.7 | 81.4 |
| 181.mcf | 4 | 126 | 66.1 | 4 | 127 | 66.0 |
| 186.crafty | 4 | 68.7 | 67.5 | 4 | 68.0 | 68.3 |
| 197.parser | 4 | 145 | 57.4 | 4 | 144 | 58.2 |
| 252.eon | 4 | 62.2 | 97.0 | 4 | 62.2 | 96.9 |
| 253.perlbmk | 4 | 93.4 | 89.4 | 4 | 93.4 | 89.4 |
| 254.gap | 4 | 71.8 | 71.1 | 4 | 71.1 | 71.8 |
| 255.vortex | 4 | 73.2 | 120 | 4 | 73.2 | 120 |
| 256.bzip2 | 4 | 136 | 51.3 | 4 | 137 | 50.7 |
| 300.twolf | 4 | 165 | 84.2 | 4 | 165 | 84.2 |

### Hardware

| | |
|---|---|
| CPU: | Intel Xeon 5110 processor (1.6 GHz, 1066 MHz system bus) |
| CPU MHz: | 1600 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Parallel: | No |
| Primary Cache: | 32KBI + 32KBD on chip, per core |
| Secondary Cache: | 4096KB(I+D) on chip, per chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 4 GB (4x1 GB DDR2 5300F, 2rank, CL5-5-5, with ECC) |
| Disk Subsystem: | Serial ATA 7200 rpm |
| Other Hardware: | |

### Software

| | |
|---|---|
| Operating System: | Windows XP, 64 bit Edition |
| Compiler: | Intel(R) C++ Compiler for 32-bit app., Version 9.1, - Build 20060519Z Package ID: W_CC_P_9.1.025<br>Microsoft Visual Studio .NET 2003 (for libraries)<br>MicroQuill SmartHeap Library, Version 8 (shlW32M.lib) |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
Portability flags:
 176.gcc:     -Dalloca=_alloca /F10000000
 186.crafty:  -DNT_i386
 253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Feedback optimization:
  +FDO: PASS1= -Qprof_gen PASS2= -Qprof_use

Baseline Tuning Flags:
 for C programs:
 -fast +FDO shlW32M.lib
 for C++ program 252.eon:
 -fast -Qcxx_features +FDO

Peak Tuning Flags:
 164.gzip:     -fast +FDO
 175.vpr:      -fast +FDO
 176.gcc:      basepeak=yes
 181.mcf:      -fast +FDO shlW32M.lib
```

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## Notes/Tuning Information (Continued)

```
 186.crafty:    -fast +FDO
 197.parser:    -fast +FDO
 252.eon:       -fast -Qcxx_features +FDO
 253.perlbmk:   -fast +FDO shlW32M.lib
 254.gap:       -fast +FDO
 255.vortex     -fast +FDO shlW32M.lib
 256.bzip2:     -fast +FDO
 300.twolf:     -fast +FDO shlW32M.lib

Extra Libraries:
 shlW32M.lib = Microquill SmartHeap Library 8
 see www.microquill.com

 Compilation was done in
 Windows XP Professional, Service Pack 2


For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
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