CFP2000 Result

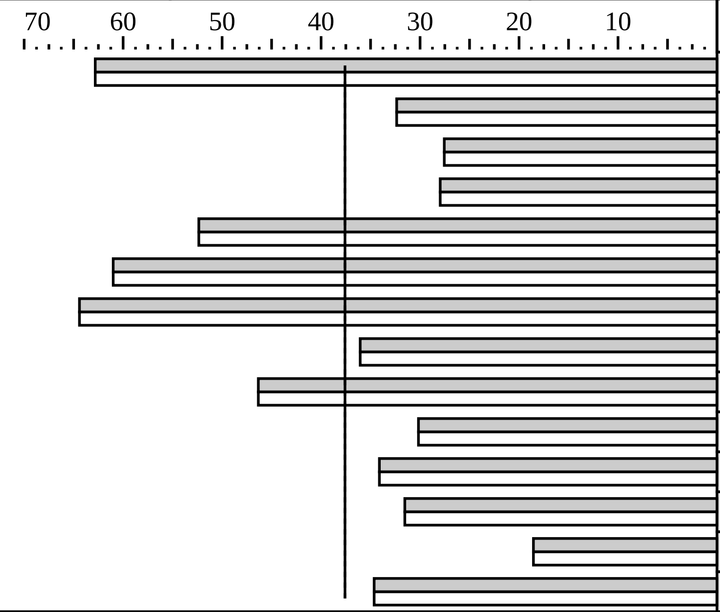
Fujitsu Siemens Computers

PRIMERGY RX100 S4, Intel Xeon processor 3050, 2.13 GHz

SPECfp_rate2000 = 37.6

SPECfp_rate_base2000 = 37.6

SPEC license #: 22	Tested by: Fujitsu Siemens Computers	Test date: Aug-2006	Hardware Avail: Sep-2006	Software Avail: Jan-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	59.1	62.8	2	59.1	62.8
171.swim	2	222	32.4	2	222	32.4
172.mgrid	2	152	27.6	2	152	27.6
173.applu	2	174	28.0	2	174	28.0
177.mesa	2	62.0	52.4	2	62.0	52.4
178.galgel	2	110	61.0	2	110	61.0
179.art	2	93.6	64.4	2	93.6	64.4
183.equake	2	83.7	36.0	2	83.7	36.0
187.facerec	2	95.1	46.3	2	95.1	46.3
188.ammp	2	169	30.2	2	169	30.2
189.lucas	2	136	34.1	2	136	34.1
191.fma3d	2	154	31.5	2	154	31.5
200.sixtrack	2	138	18.5	2	138	18.5
301.apsi	2	174	34.6	2	174	34.6

Hardware

CPU:	Intel Xeon processor 3050 (2.13 GHz, 2MB L2, 1066 MHz system bus)
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x2GB DDRII-RAM PC2-4200E (CAS 4-4-4)
Disk Subsystem:	Seagate ST3146854SS (SAS, 15.4krpm, 146GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.0 for EM64T Build 20060120 (for 64-bit applications)
File System:	ext3
System State:	Multi-user run level 3

Notes/Tuning Information

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GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

Notes/Tuning Information (Continued)

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The system bus runs at 1066 MHz

This result was measured with 64-bit binaries using the 64-bit version of
the operating system.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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