CFP2000 Result

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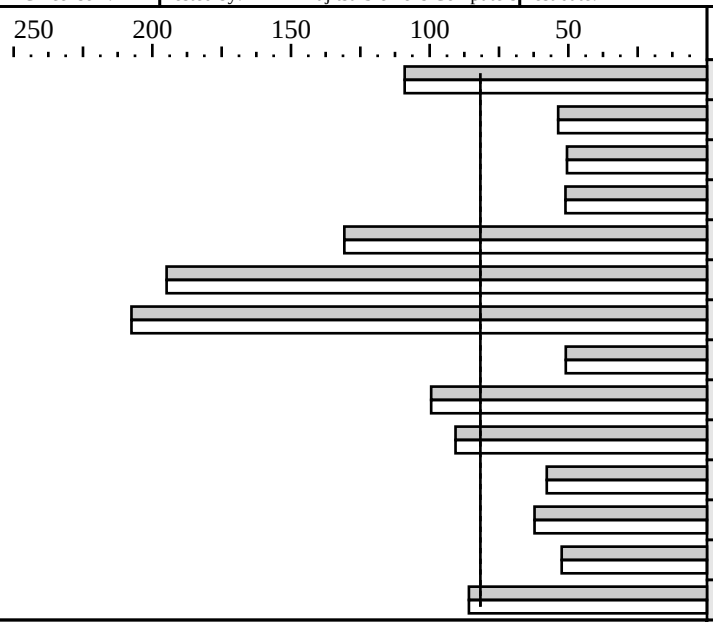
Fujitsu Siemens Computers

PRIMERGY RX600 S3, Intel Xeon processor 7120M, 3.0 GHz

SPECfp_rate2000 = 81.7

SPECfp_rate_base2000 = 81.7

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Aug-2006 | Hardware Avail: Aug-2006 | Software Avail: Feb-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	136	109	8	136	109
171.swim	8	536	53.7	8	536	53.7
172.mgrid	8	331	50.5	8	331	50.5
173.applu	8	382	51.0	8	382	51.0
177.mesa	8	99.4	131	8	99.4	131
178.galgel	8	138	195	8	138	195
179.art	8	116	208	8	116	208
183.equake	8	237	50.9	8	237	50.9
187.facerec	8	177	99.5	8	177	99.5
188.ammp	8	225	90.7	8	225	90.7
189.lucas	8	321	57.8	8	321	57.8
191.fma3d	8	313	62.2	8	313	62.2
200.sixtrack	8	195	52.4	8	195	52.4
301.apsi	8	281	85.8	8	281	85.8

Hardware

CPU:	Intel Xeon processor 7120M (3.0 GHz, 2x1MB L2, 4MB L3, 800 MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2,4 chips
Parallel:	No
Primary Cache:	12k micro-ops(I) + 16KB(D) on chip, per core
Secondary Cache:	1024KB(I+D) on chip, per core
L3 Cache:	4MB on chip, per chip
Other Cache:	N/A
Memory:	16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)
Disk Subsystem:	Fujitsu MAS3367NC (SCSI, 15krpm, 36GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 9 with SP3 Kernel 2.6.5-7.244-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.0 for EM64T Build 20060120 (for 64-bit applications)
File System:	ext3
System State:	Multi-user run level 3

Notes/Tuning Information

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GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

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Notes/Tuning Information (Continued)

The system bus runs at 800 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

This result was measured on the PRIMERGY TX600 S3. The PRIMERGY TX600 S3 and the PRIMERGY RX600 S3 are electronically equivalent.

BIOS Configuration:
Adjacent Sector Prefetch = Disable

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