CINT2000 Result

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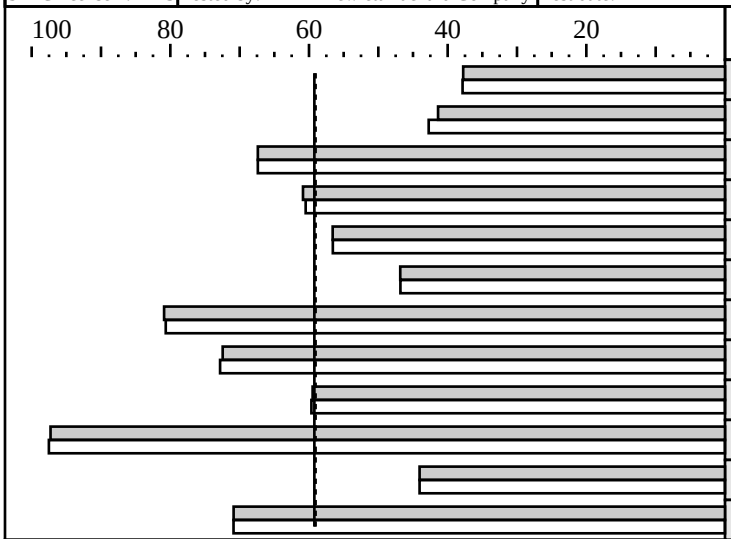
Hewlett-Packard Company

ProLiant DL320 G5 (2.66 GHz, Intel Xeon processor 3070)

SPECint_rate2000 = 59.2

SPECint_rate_base2000 = 59.1

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Sep-2006 | Hardware Avail: Sep-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	86.0	37.8	2	85.8	37.9
175.vpr	2	78.5	41.4	2	76.0	42.7
176.gcc	2	37.9	67.4	2	37.9	67.4
181.mcf	2	68.6	60.9	2	69.0	60.5
186.crafty	2	41.0	56.6	2	41.0	56.6
197.parser	2	89.2	46.8	2	89.2	46.8
252.eon	2	37.3	80.9	2	37.4	80.7
253.perlbmk	2	57.6	72.4	2	57.3	72.8
254.gap	2	42.9	59.5	2	42.8	59.7
255.vortex	2	45.3	97.3	2	45.2	97.5
256.bzip2	2	79.0	44.1	2	79.0	44.1
300.twolf	2	98.2	70.9	2	98.2	70.9

Hardware

CPU:	Intel Xeon processor 3070 (2.66 GHz, 4 MB L2 shared, 1066 MHz bus)
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 GB (4x2 GB PC2-5300E)
Disk Subsystem:	1x250 GB SATA
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
```

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Notes/Tuning Information (Continued)

```
    256.bzip2:    basepeak=1
    300.twolf:    -fast                +FDO              shlw32M.lib
  BIOS Configuration Notes
    Power Regulator set to Static High
    Adjacent Sector Prefetch disabled
```