CFP2000 Result

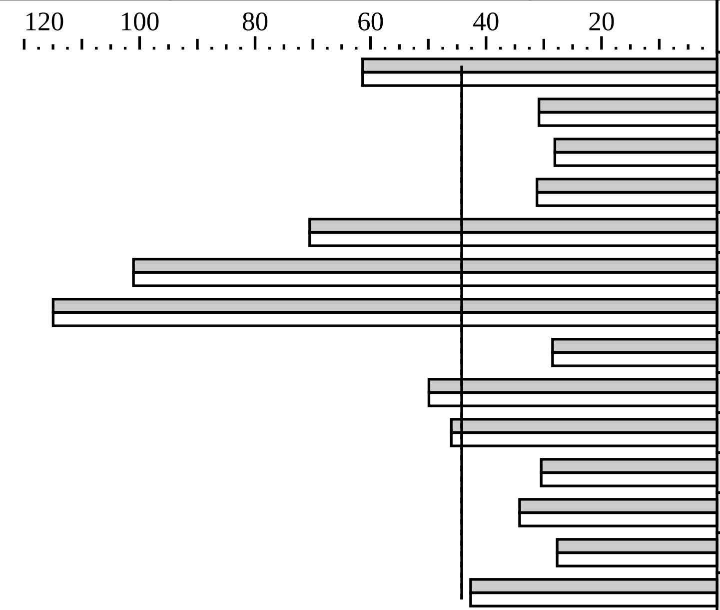
Acer Incorporated

Acer Altos R520 (3.0 GHz Intel Xeon Processor 5050)

SPECfp_rate2000 = 44.2

SPECfp_rate_base2000 = 44.2

SPEC license #: 97	Tested by: Acer Incorporated	Test date: May-2006	Hardware Avail: Nov-2006	Software Avail: Dec-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	121	61.4	4	121	61.4
171.swim	4	466	30.8	4	466	30.8
172.mgrid	4	297	28.1	4	297	28.1
173.applu	4	312	31.2	4	312	31.2
177.mesa	4	92.1	70.5	4	92.1	70.5
178.galgel	4	133	101	4	133	101
179.art	4	105	115	4	105	115
183.equake	4	212	28.5	4	212	28.5
187.facerec	4	177	49.9	4	177	49.9
188.ammp	4	222	46.0	4	222	46.0
189.lucas	4	305	30.4	4	305	30.4
191.fma3d	4	285	34.2	4	285	34.2
200.sixtrack	4	184	27.7	4	184	27.7
301.apsi	4	283	42.7	4	283	42.7

Hardware

CPU:	Intel Xeon Processor 5050 (3.0 GHz, 2x2MB L2, 667MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 x 2048MB ECC FB-DIMM 667
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	

Software

Operating System:	Red Hat Enterprise Linux 4 Advanced Server Update 3 EM64T (Kernel 2.6.9-34.ELsmp)
Compiler:	Intel C++ Compiler 9.0 Build 20051201 for EM64T Intel Fortran Compiler 9.0 Build 20051201 for EM64T
File System:	ext3
System State:	Runlevel 3

Notes/Tuning Information

```
GENERAL
 ONESTEP=yes for all benchmarks
 +FDO :  PASS1= -prof_gen   PASS2= -prof_use
PORTABILITY FLAGS
 -DSPEC_CPU2000_LP64 applied to all benchmarks
 178.galgel: -FI for fixed-format Fortran
BASE TUNING
 Baseline optimizations for C and Fortran: -fast +FDO
PEAK TUNING
 basepeak=yes set for all benchmarks
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
This result was measured on the Acer Alto R720.
The Altos R520 and Altos R720 are electronically equivalent.
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