CINT2000 Result

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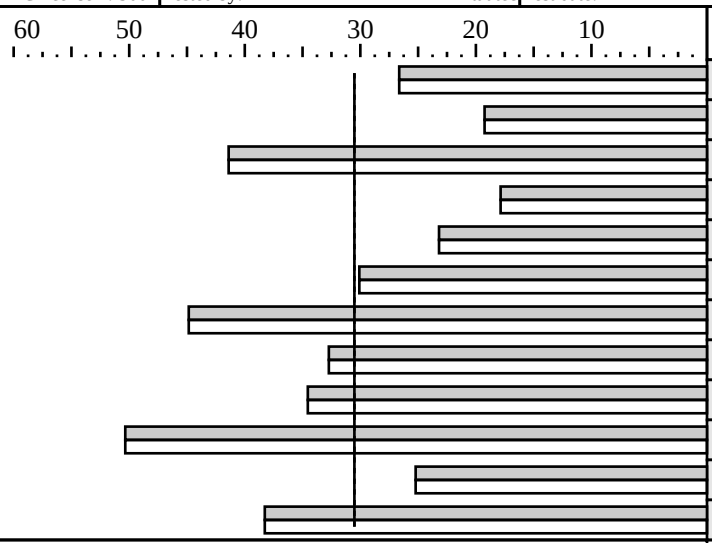
Itautec

Servidor Itautec LX201 (Intel Xeon 5030 processor)

SPECint_rate2000 = 30.5

SPECint_rate_base2000 = 30.5

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	244	26.6	4	244	26.6
175.vpr	4	337	19.2	4	337	19.2
176.gcc	4	123	41.4	4	123	41.4
181.mcf	4	468	17.9	4	468	17.9
186.crafty	4	200	23.2	4	200	23.2
197.parser	4	278	30.1	4	278	30.1
252.eon	4	135	44.8	4	135	44.8
253.perlbmk	4	255	32.7	4	255	32.7
254.gap	4	148	34.5	4	148	34.5
255.vortex	4	175	50.3	4	175	50.3
256.bzip2	4	276	25.2	4	276	25.2
300.twolf	4	364	38.3	4	364	38.3

Hardware

CPU:	Intel Xeon 5030 processor (2.66GHz, 2x2MB L2, 667 MHz system bus)
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x1024MB + 4x512MB FB-DIMM DDR2 SDRAM ECC CL5 667MHz
Disk Subsystem:	1 x 200GB Serial ATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060519Z (for 32-bit applications), Microsoft Visual Studio 2003(for libraries), MicroQuill SmartHeap for SMP Library 8.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
PORTABILITY FLAGS
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafy:   -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
GENERAL
   +FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning flags
   for C programs:   -fast +FDO shlSMPMt.lib
   for C++ programs: -fast -Qcxx_features +FDO
EXTRA LIBRARIES
   MicroQuill SmartHeap for SMP Library 8.0 available from
   http://www.microquill.com/
```