CINT2000 Result

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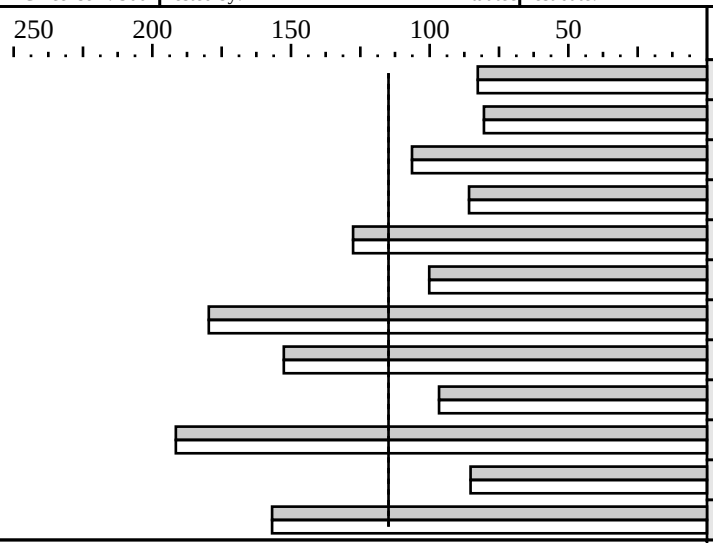
Itautec

Servidor Itautec LX201 (Intel Xeon 5160 processor)

SPECint_rate2000 = 115

SPECint_rate_base2000 = 115

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	78.6	82.7	4	78.6	82.7
175.vpr	4	80.8	80.4	4	80.8	80.4
176.gcc	4	48.0	106	4	48.0	106
181.mcf	4	97.3	85.8	4	97.3	85.8
186.crafty	4	36.4	128	4	36.4	128
197.parser	4	83.4	100	4	83.4	100
252.eon	4	33.6	180	4	33.6	180
253.perlbmk	4	54.7	153	4	54.7	153
254.gap	4	52.8	96.6	4	52.8	96.6
255.vortex	4	46.0	192	4	46.0	192
256.bzip2	4	81.6	85.3	4	81.6	85.3
300.twolf	4	88.8	157	4	88.8	157

Hardware

CPU:	Intel Xeon 5160 processor (3.00 GHz, 4MB L2 shared, 1333 MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4096KB(I+D) on chip, per chip, shared
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x2GB DDR2-RAM PC2-5300F (CAS 5-5-5)ECC
Disk Subsystem:	1 x 120GB Serial ATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060519Z (for 32-bit applications), Microsoft Visual Studio .NET 7.0.9466 (for libraries), MicroQuill SmartHeap for SMP Library 8.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

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PORTABILITY FLAGS
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafy:   -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
GENERAL
   +FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning flags
   for C programs:   -fast +FDO shlSMPMt.lib
   for C++ programs: -fast -Qcxx_features +FDO
EXTRA LIBRARIES
   MicroQuill SmartHeap for SMP Library 8.0 available from
   http://www.microquill.com/
```