CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

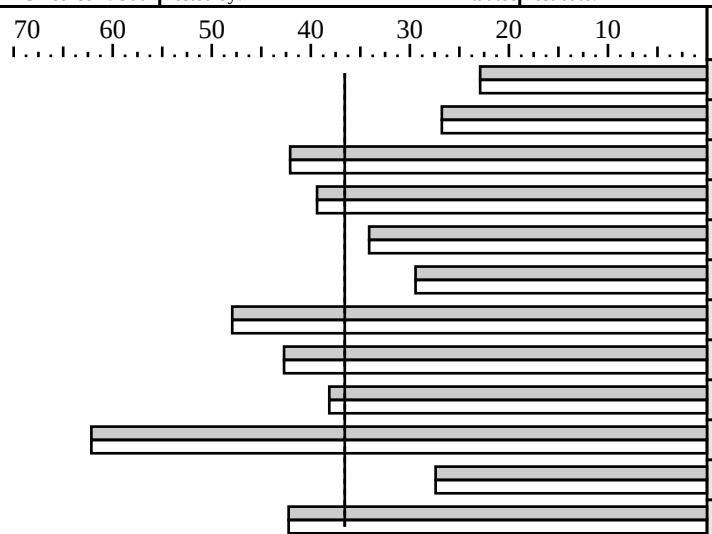
Itautec

Servidor Itautec MX221 (Intel Xeon 5110 processor, 1.86GHz)

SPECint_rate2000 = 36.6

SPECint_rate_base2000 = 36.6

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	142	22.9	2	142	22.9
175.vpr	2	121	26.8	2	121	26.8
176.gcc	2	60.6	42.1	2	60.6	42.1
181.mcf	2	106	39.4	2	106	39.4
186.crafty	2	68.0	34.1	2	68.0	34.1
197.parser	2	142	29.4	2	142	29.4
252.eon	2	62.9	47.9	2	62.9	47.9
253.perlbmk	2	97.8	42.7	2	97.8	42.7
254.gap	2	66.9	38.1	2	66.9	38.1
255.vortex	2	70.9	62.2	2	70.9	62.2
256.bzip2	2	127	27.4	2	127	27.4
300.twolf	2	165	42.2	2	165	42.2

Hardware

CPU:	Intel Xeon 5110 processor (1.60 GHz, 4MB L2 shared, 1066 MHz system bus)
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4096KB(I+D) on chip, per chip, shared
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)ECC
Disk Subsystem:	1 x 36GB U320 SCSI 10000rpm
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060519Z (for 32-bit applications), Microsoft Visual Studio .NET 7.0.9466 (for libraries), MicroQuill SmartHeap for SMP Library 8.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
PORTABILITY FLAGS
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafy:   -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
GENERAL
   +FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning flags
   for C programs:   -fast -QxP +FDO shlSMPMt.lib
   for C++ programs: -fast -Qcxx_features +FDO
EXTRA LIBRARIES
   MicroQuill SmartHeap for SMP Library 8.0 available from
   http://www.microquill.com/
```