Fujitsu Siemens Computers
PRIMERGY RX300 S3, Intel Xeon processor X5355, 2.66 GHz

NOTE: SPECfp_rate2000 = 60.8
SPECfp_rate_base2000 = 60.8

**Benchmark** 
**Base Copies** | **Base Runtime** | **Base Ratio** | **Copies** | **Runtime** | **Ratio**
--- | --- | --- | --- | --- | ---
168.wupwise | 4 | 98.7 | 75.2 | 4 | 98.7 | 75.2
171.swim | 4 | 425 | 33.8 | 4 | 425 | 33.8
172.mgrid | 4 | 266 | 31.4 | 4 | 266 | 31.4
173.aplu | 4 | 291 | 33.5 | 4 | 291 | 33.5
177.mesa | 4 | 53.5 | 121 | 4 | 53.5 | 121
178.galgel | 4 | 75.3 | 179 | 4 | 75.3 | 179
179.art | 4 | 68.2 | 177 | 4 | 68.2 | 177
183.equake | 4 | 184 | 32.8 | 4 | 184 | 32.8
187.facerec | 4 | 111 | 79.7 | 4 | 111 | 79.7
188.ammp | 4 | 127 | 80.4 | 4 | 127 | 80.4
189.lucas | 4 | 251 | 37.0 | 4 | 251 | 37.0
191.fma3d | 4 | 229 | 42.5 | 4 | 229 | 42.5
200.sixtrack | 4 | 110 | 46.5 | 4 | 110 | 46.5
301.apsi | 4 | 201 | 60.0 | 4 | 201 | 60.0

**Hardware**
- **CPU:** Intel Xeon processor X5355 (2.66 GHz, 2x4MB L2, 1333 MHz system bus)
- **CPU MHz:** 2666
- **FPU:** Integrated
- **CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip
- **CPU(s) orderable:** 1,2 chips
- **Parallel:** No
- **Primary Cache:** 32KB(I) + 32KB(D) on chip, per core
- **Secondary Cache:** 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
- **L3 Cache:** N/A
- **Other Cache:** N/A
- **Memory:** 8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
- **Disk Subsystem:** Seagate ST336754SS (SAS, 15.4k rpm, 36GB)
- **Other Hardware:** none

**Software**
- **Operating System:** 64-Bit SUSE LINUX Enterprise Server 10
- **Compiler:** Intel C++ and Fortran Compiler 9.1 for EM64T
- **File System:** ext2
- **System State:** Multi-user runlevel 3

**Notes/Tuning Information**

**GENERAL**
+FDO implies feedback-directed optimization
PASS1: -prof_gen PASS2: -prof_use

**Optimization flags**
ONESTEP=yes set for all benchmarks

**Portability flags**
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

**Base tuning flags**
for Fortran and C programs: -fast +FDO

**Peak tuning flags**
same as baseline (basepeak=true set globally)
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Notes/Tuning Information (Continued)

The system bus runs at 1333 MHz

This result was measured with 64-bit binaries using the 64-bit version of
the operating system.

BIOS Configuration:
Adjacent Sector Prefetch = disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3
are electronically equivalent.

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