CFP2000 Result

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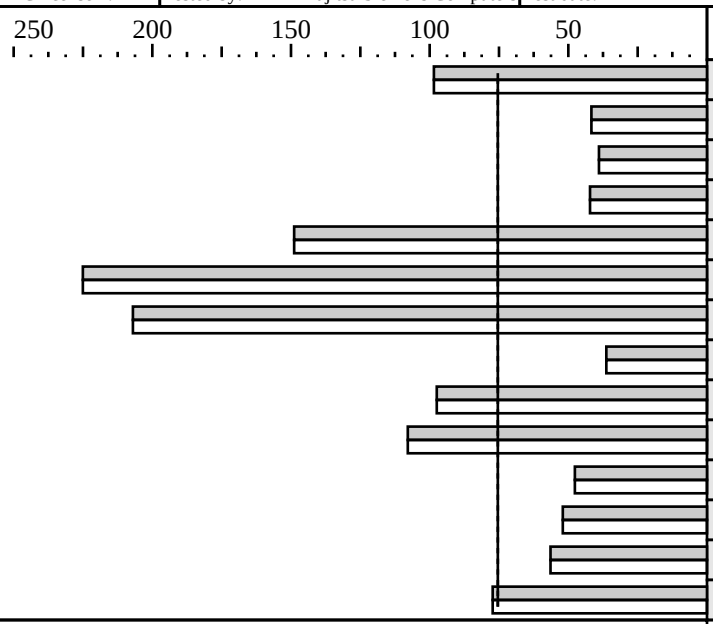
Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor E5310, 1.60 GHz

SPECfp_rate2000 = 75.4

SPECfp_rate_base2000 = 75.4

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Oct-2006 | Hardware Avail: Nov-2006 | Software Avail: Aug-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	151	98.5	8	151	98.5
171.swim	8	690	41.7	8	690	41.7
172.mgrid	8	429	39.0	8	429	39.0
173.applu	8	462	42.2	8	462	42.2
177.mesa	8	87.3	149	8	87.3	149
178.galgel	8	120	225	8	120	225
179.art	8	117	207	8	117	207
183.equake	8	332	36.3	8	332	36.3
187.facerec	8	181	97.4	8	181	97.4
188.ammp	8	189	108	8	189	108
189.lucas	8	390	47.6	8	390	47.6
191.fma3d	8	375	52.0	8	375	52.0
200.sixtrack	8	181	56.4	8	181	56.4
301.apsi	8	312	77.3	8	312	77.3

Hardware

CPU:	Intel Xeon processor E5310 (1.60 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-4200F (CAS 4-4-4)
Disk Subsystem:	Seagate ST336754SS (SAS, 15.4krpm, 36GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.1 for EM64T Build 20060816 (for 64-bit applications)
File System:	ext2
System State:	Multi-user runlevel 3

Notes/Tuning Information

```
GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

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Notes/Tuning Information (Continued)

The system bus runs at 1066 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:
Adjacent Sector Prefetch = disable, Hardware Prefetch = disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY TX300 S3 and the PRIMERGY RX300 S3 are electronically equivalent.

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