CFP2000 Result

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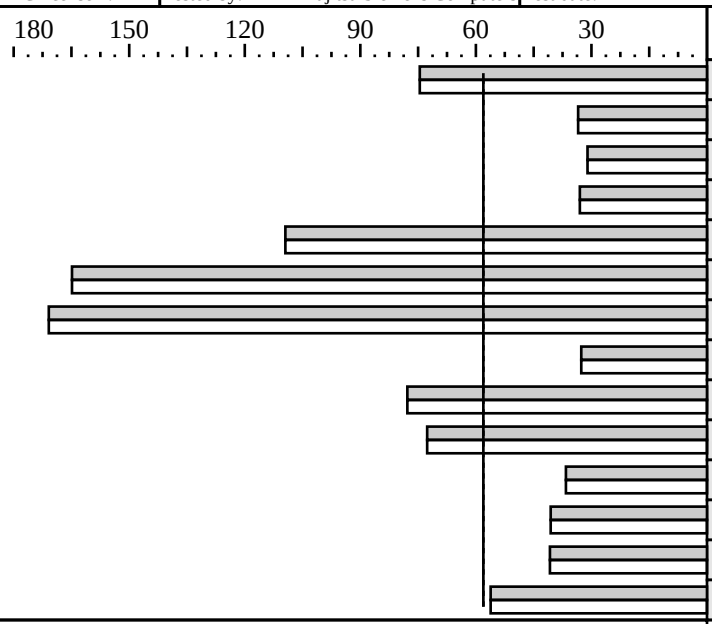
Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor E5345, 2.33 GHz

SPECfp_rate2000 = 58.1

SPECfp_rate_base2000 = 58.1

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Oct-2006 | Hardware Avail: Jan-2007 | Software Avail: Aug-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	99.6	74.6	4	99.6	74.6
171.swim	4	430	33.5	4	430	33.5
172.mgrid	4	269	31.0	4	269	31.0
173.applu	4	295	33.0	4	295	33.0
177.mesa	4	59.3	109	4	59.3	109
178.galgel	4	81.6	165	4	81.6	165
179.art	4	70.6	171	4	70.6	171
183.equake	4	185	32.6	4	185	32.6
187.facerec	4	113	77.8	4	113	77.8
188.ammp	4	141	72.6	4	141	72.6
189.lucas	4	253	36.6	4	253	36.6
191.fma3d	4	240	40.6	4	240	40.6
200.sixtrack	4	125	40.8	4	125	40.8
301.apsi	4	215	56.2	4	215	56.2

Hardware

CPU:	Intel Xeon processor E5345 (2.33 GHz, 2x4MB L2, 1333 MHz system bus)
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem:	Seagate ST336754SS (SAS, 15.4krpm, 36GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.1 for EM64T Build 20060816 (for 64-bit applications)
File System:	ext2
System State:	Multi-user runlevel 3

Notes/Tuning Information

```
GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

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Notes/Tuning Information (Continued)

The system bus runs at 1333 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:
Adjacent Sector Prefetch = disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY TX300 S3 and the PRIMERGY RX300 S3 are electronically equivalent.

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