CINT2000 Result

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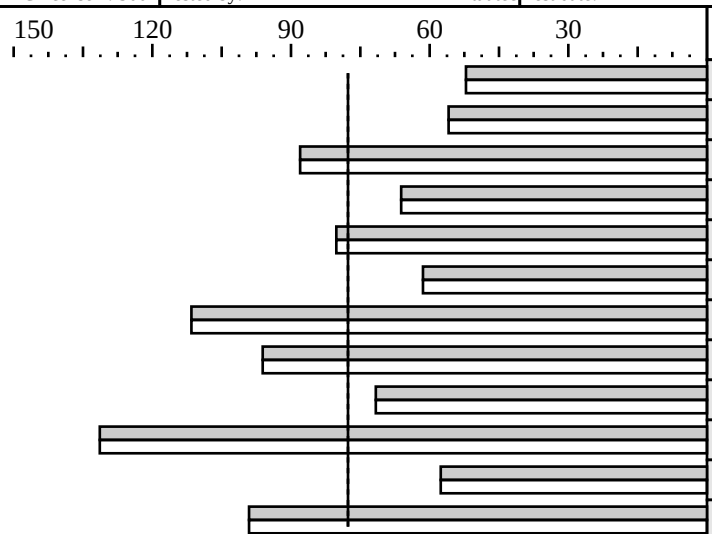
Itautec

Servidor Itautec MX201 (Intel Xeon 5120 processor, 1.86GHz)

SPECint_rate2000 = 77.7

SPECint_rate_base2000 = 77.7

SPEC license #: 9001 | Tested by: Itautec | Test date: Nov-2006 | Hardware Avail: Jul-2006 | Software Avail: Mar-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	125	52.1	4	125	52.1
175.vpr	4	116	55.9	4	116	55.9
176.gcc	4	58.0	88.0	4	58.0	88.0
181.mcf	4	126	66.2	4	126	66.2
186.crafty	4	57.9	80.2	4	57.9	80.2
197.parser	4	136	61.4	4	136	61.4
252.eon	4	54.1	112	4	54.1	112
253.perlbmk	4	86.9	96.1	4	86.9	96.1
254.gap	4	71.2	71.7	4	71.2	71.7
255.vortex	4	67.1	131	4	67.1	131
256.bzip2	4	121	57.6	4	121	57.6
300.twolf	4	141	99.1	4	141	99.1

Hardware

CPU:	Intel Xeon 5120 processor (1.86 GHz, 4MB L2 shared, 1066 MHz system bus)
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4MB (I+D) on chip, per chip, shared
L3 Cache:	N/A
Other Cache:	N/A
Memory:	14GB (7x2GB DDR2-RAM PC2-5300 FB-DIMM CL5 ECC)
Disk Subsystem:	1 x 120GB Serial ATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications), Microsoft Visual Studio 2005(for libraries), Microquill SmartHeap Library for SMP v.8.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
PORTABILITY FLAGS
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafty:  -DNT_i386
   252.eon:     -DHAS_ERRLIST
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
GENERAL
   +FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Base tuning flags
   for C programs:   -fast                +FDO shlsmpmt.lib
   for C++ programs: -fast -Qcxx_features +FDO

  BIOS settings default.
EXTRA LIBRARIES
  MicroQuill SmartHeap Library for SMP v.8.0
  http://www.microquill.com/
```