CINT2000 Result

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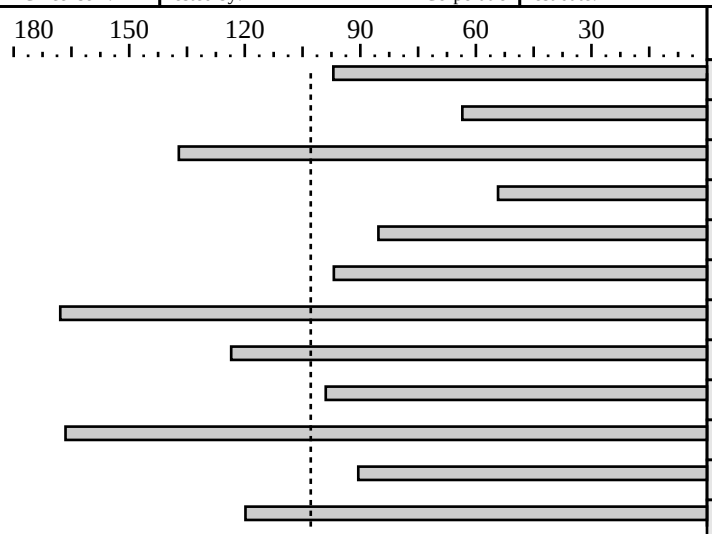
IBM Corporation

IBM System X 3800 (2.5 GHz Xeon 7110N)

SPECint_rate2000 = --

SPECint_rate_base2000 = 103

SPEC license #: 11 | Tested by: IBM Corporation | Test date: Dec-2006 | Hardware Avail: Oct-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	16	268	97.0			
175.vpr	16	409	63.5			
176.gcc	16	149	137			
181.mcf	16	616	54.2			
186.crafty	16	218	85.3			
197.parser	16	345	96.9			
252.eon	16	144	168			
253.perlbmk	16	270	124			
254.gap	16	206	99.0			
255.vortex	16	212	167			
256.bzip2	16	308	90.5			
300.twolf	16	465	120			

Hardware

CPU:	Intel Xeon processor 7110N (2.5 GHz, 667 MHz bus)
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1, 2, 4 chips
Parallel:	No
Primary Cache:	12 K I micro-ops + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	16 MB on chip per chip
Other Cache:	N/A
Memory:	16 x 2048 MB ECC PC2-3200
Disk Subsystem:	73GB SAS 15K RPM
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler 9.1 for 32-bit applications Build 20060323Z Package ID: W_CC_P_9.1.020 Microsoft Visual Studio 2005(for libraries) SmartHeap Library Version 8.0 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M-80.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M-80.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:     -DHAS_ERRLIST
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Bios Settings
   Hardware Prefetch enabled
   Memory Array set to High-Performance Memory Array
```