CINT2000 Result

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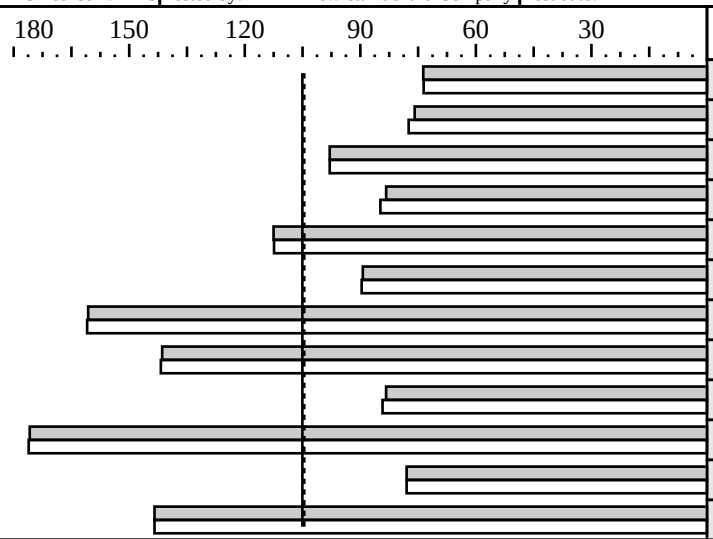
Hewlett-Packard Company

ProLiant BL480c (2.66 GHz, Intel Xeon processor X5355)

SPECint_rate2000 = 105

SPECint_rate_base2000 = 105

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jan-2007 | Hardware Avail: Jan-2007 | Software Avail: Nov-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	88.2	73.7	4	88.3	73.6
175.vpr	4	85.6	75.9	4	83.9	77.4
176.gcc	4	52.1	97.9	4	52.1	97.9
181.mcf	4	100	83.3	4	98.5	84.8
186.crafty	4	41.2	113	4	41.3	112
197.parser	4	93.5	89.4	4	93.2	89.7
252.eon	4	37.5	161	4	37.5	161
253.perlbmk	4	59.0	141	4	58.9	142
254.gap	4	61.3	83.3	4	60.6	84.2
255.vortex	4	50.1	176	4	50.1	176
256.bzip2	4	89.3	78.0	4	89.3	78.0
300.twolf	4	97.0	143	4	97.0	143

Hardware

CPU:	Intel Xeon processor X5355 (2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus)
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip (4 MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	1x72 GB 10 K SAS
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise X64 Edition (x86_64) SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20061103Z) Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries) MicroQuill Smartheap Library 8.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
  256.bzip2:    basepeak=1
```

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Notes/Tuning Information (Continued)

```
  300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch disabled
Other Configuration Notes
  The start /b /wait /affinity command is used to bind CPU(s) to processes.
```