CINT2000 Result

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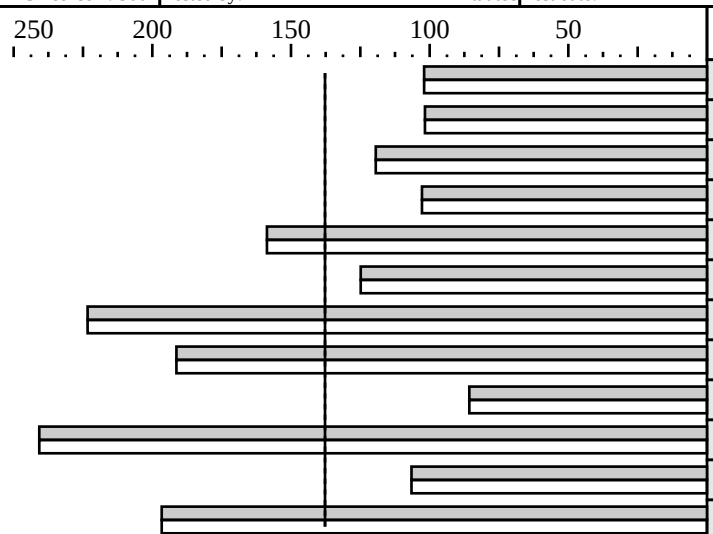
Itautec

Servidor Itautec LX201 (Intel Xeon processor E5320, 1.86 GHz)

SPECint_rate2000 = 138

SPECint_rate_base2000 = 138

SPEC license #: 9001 | Tested by: Itautec | Test date: Feb-2007 | Hardware Avail: Feb-2007 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	127	102	8	127	102
175.vpr	8	128	102	8	128	102
176.gcc	8	85.5	119	8	85.5	119
181.mcf	8	163	103	8	163	103
186.crafty	8	58.5	159	8	58.5	159
197.parser	8	134	125	8	134	125
252.eon	8	54.0	223	8	54.0	223
253.perlbmk	8	87.3	191	8	87.3	191
254.gap	8	119	85.7	8	119	85.7
255.vortex	8	73.2	241	8	73.2	241
256.bzip2	8	131	107	8	131	107
300.twolf	8	142	197	8	142	197

Hardware

CPU:	Intel Xeon processor E5320 (1.86 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8 MB I+D on chip per chip (4 MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem:	1 x 120GB SATA 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060519Z (for 32-bit applications), Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries) Microquill SmartHeap Library v.8.0 for SMP
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
GENERAL
Basepeak=yes is used for all programs
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability flags
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafty:  -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Base tuning flags
   for C programs:   -fast +FDO shlSMPMt.lib
   for C++ programs: -fast -Qcxx_features +FDO  shlSMPMt.lib
EXTRA LIBRARIES
  MicroQuill SmartHeap Library v.8.0 for SMP
  http://www.microquill.com
```