CINT2000 Result

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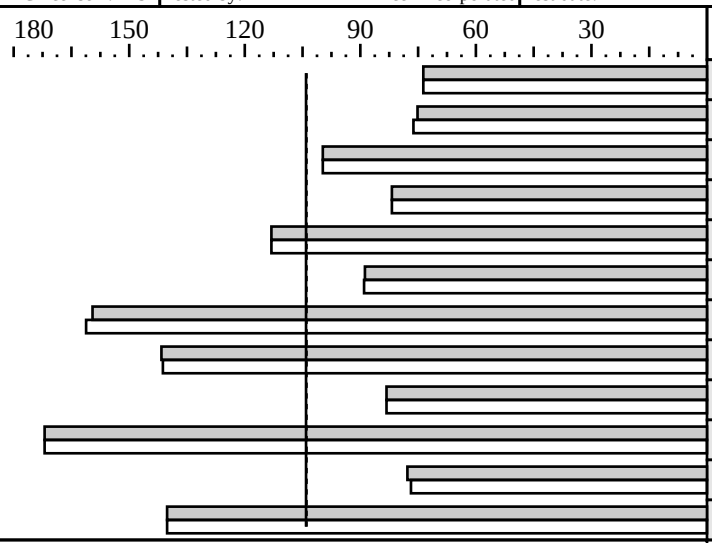
Acer Incorporated

Acer Altos R520 (2.66 GHz Intel Xeon Processor X5355)

SPECint_rate2000 = 104

SPECint_rate_base2000 = 104

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Dec-2006 | Hardware Avail: Nov-2006 | Software Avail: Nov-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	88.2	73.6	4	88.2	73.6
175.vpr	4	86.4	75.2	4	85.2	76.2
176.gcc	4	51.2	99.7	4	51.2	99.7
181.mcf	4	102	81.8	4	102	81.8
186.crafty	4	41.0	113	4	41.0	113
197.parser	4	94.0	88.8	4	93.8	89.0
252.eon	4	37.8	160	4	37.4	161
253.perlbmk	4	59.0	142	4	59.1	141
254.gap	4	61.3	83.2	4	61.3	83.2
255.vortex	4	51.3	172	4	51.3	172
256.bzip2	4	89.5	77.8	4	90.6	76.9
300.twolf	4	99.3	140	4	99.3	140

Hardware

CPU:	Intel Xeon Processor X5355 (2.66 GHz, 8MB L2, 1333MHz system bus)
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 x 2048MB ECC FB-DIMM DDR2-667
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z for IA32 Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

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+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
256.bzip2:   -fast -Oa -Qunroll1 +FDO
```

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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlW32M.lib
Enable hardware prefetching and disable adjacent cache line prefetch BIOS setting
This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.