CINT2000 Result

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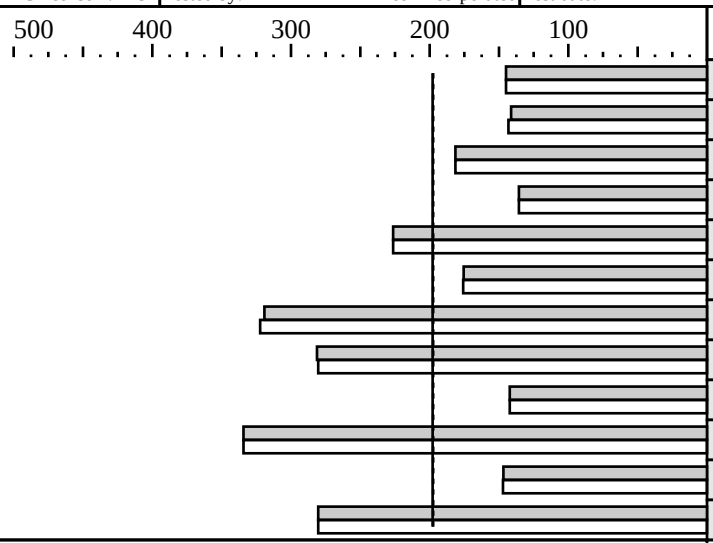
Acer Incorporated

Acer Altos R720 (2.66 GHz Intel Xeon Processor X5355)

SPECint_rate2000 = 198

SPECint_rate_base2000 = 197

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Dec-2006 | Hardware Avail: Nov-2006 | Software Avail: Nov-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	89.6	145	8	89.6	145
175.vpr	8	92.0	141	8	90.7	143
176.gcc	8	56.3	181	8	56.3	181
181.mcf	8	123	136	8	123	136
186.crafty	8	41.0	226	8	41.0	226
197.parser	8	95.2	175	8	95.0	176
252.eon	8	37.8	319	8	37.4	322
253.perlbmk	8	59.4	281	8	59.6	280
254.gap	8	71.8	142	8	71.8	142
255.vortex	8	52.8	334	8	52.8	334
256.bzip2	8	94.8	147	8	94.6	147
300.twolf	8	99.3	280	8	99.3	280

Hardware

CPU:	Intel Xeon Processor X5355 (2.66 GHz, 8MB L2, 1333MHz system bus)
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 x 2048MB ECC FB-DIMM DDR2-667
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z for IA32 Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
256.bzip2:   -fast -Oa -Qunroll1 +FDO
```

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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlW32M.lib
Enable hardware prefetching and disable adjacent cache line prefetch BIOS setting
This result was measured on the Acer Altos R520.